

Optimization and Performance Analysis of Inorganic Lead-Free CsSnBr₃ Perovskite Solar Cells Using Diverse Electron Transport Materials

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Cite This: *Energy Fuels* 2024, 38, 8229–8248



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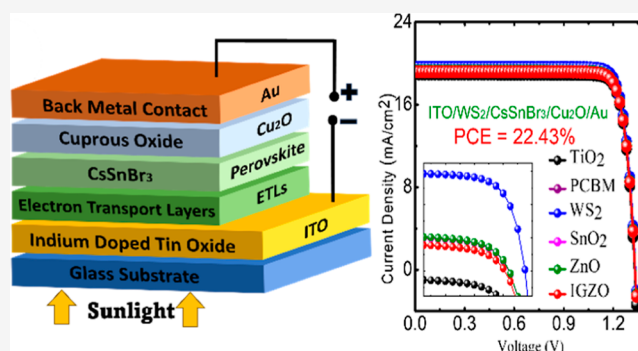
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ABSTRACT: The rapid enhancement in the power conversion efficiency of perovskite solar cells (PSCs) within a short time frame has introduced challenges related to the instability and toxicity linked with lead (Pb), impeding their progress toward commercial viability. In response, the exploration of Pb-free alternatives, such as CsSnBr₃, has garnered substantial research attention. CsSnBr₃ stands out as a highly promising contender for fulfilling the role of an absorber layer in solar cell technology, distinguished by its economical nature, robust stability, and impressive efficiency. In this study, CsSnBr₃ is employed as an absorber layer, Cu₂O as the hole transport layer (HTL), and TiO₂, PCBM, WS₂, ZnO, SnO₂, and IGZO as electron transport layers (ETLs). This work focuses on enhancing the photovoltaic (PV) performance parameters of CsSnBr₃-based PSCs using SCAPS-1D numerical simulation. This is achieved by optimizing characteristics (thickness, doping, and defect density) of the light absorber layer, ETLs, and HTL. Additionally, the effects of doping and defect density (N_d) on the PV performance at CsSnBr₃/ETL and HTL/CsSnBr₃ interfaces are investigated. The device architecture's performance was significantly influenced by the absorber layer's thickness, acceptor density, defect density, and the combination of various ETLs and HTLs. Optimized devices employing TiO₂, PCBM, WS₂, ZnO, SnO₂, and IGZO ETLs exhibited PCEs of 21.64, 21.94, 22.43, 21.94, 21.94, and 21.88%, respectively. Moreover, the study included the illustration of capacitance effects and Mott–Schottky (M–S) analysis for the six optimized devices, complemented by the computation of corresponding $J-V$ and QE characteristics. The PV performance obtained from this thorough analysis is then compared with previously published theoretical and experimental results for CsSnBr₃-based PSCs. The performance of the top six devices is validated and compared using the wxAMPS simulation tool. By employing a variety of analytical techniques and theoretical simulations, an optimized configuration is developed, leading to the achievement of the highest reported efficiency to date at 22.43% using any simulation method for indium-doped tin oxide/TiO₂/CsSnBr₃/Cu₂O/Au configuration.



1. INTRODUCTION

The perovskite solar cell (PSC) has emerged as a highly promising photovoltaic technology, achieving a power conversion efficiency (PCE) that surpasses 25% in a single-junction architecture. This impressive level of efficiency positions PSCs as formidable competitors to traditional silicon solar cells. Remarkably, PSCs are on the verge of commercialization, marking a significant milestone within just over a decade since their inception.^{1,2} However, the journey of PSCs toward industrialization faces significant hurdles, primarily revolving around the stability of the device in ambient air under illumination and concerns regarding the toxicity of the materials employed.^{3–8} To date, the most effective formulation for PSCs comprises a synergistic blend of methylammonium, formamidinium (FA), and cesium (Cs⁺) strategically placed in the A cation site.^{9–13} Simultaneously, lead (Pb) is positioned in the B cation site, while iodine (I),

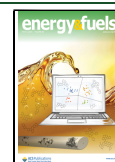
chlorine (Cl), and bromine (Br) are integrated into the X anion site within the well-defined ABX₃ perovskite structure.¹⁴ Nevertheless, the presence of toxic and heavy metals, particularly lead, throughout the lifecycle of PSCs raises significant environmental concerns.^{6–8} Despite the high efficiency achieved, the skepticism from both research communities and the industry regarding its future has led to a growing interest in Pb-free perovskite materials. Consequently, researchers are actively exploring alternatives to Pb-containing perovskites for SC applications. Several Pb-free

Received: February 29, 2024

Revised: April 4, 2024

Accepted: April 4, 2024

Published: April 19, 2024



perovskite absorber materials are characterized by wide bandgaps, making them viable candidates for replacing their hazardous lead-containing counterparts. The pioneering studies on CsSnX₃ perovskites were documented approximately between 2014 and 2017.^{15–17} Tin-based perovskite absorber materials are gaining prominence in the research sector due to their properties, which closely resemble those of lead halide perovskites and exhibit high PCE. Nevertheless, a significant challenge that requires attention is the degradation of these devices in ambient atmospheric conditions. In the realm of solar applications, prominent Sn-based perovskites include MASnI₃ (1.3 eV), FASnI₃ (1.41 eV), CsSnI₃ (1.3 eV), and CsSnBr₃ (1.75–1.80 eV).^{11,18–23} Among these, CsSnBr₃ received less attention, primarily because of its wider bandgap,³⁹ making it a less efficient light absorber for PSCs compared to the other three. Despite this, CsSnBr₃ demonstrated greater robustness than CsSnI₃. The Pb-free alternative, CsSnBr₃, exhibits competitive semiconductor properties, featuring a direct bandgap,²⁰ improved thermal stability,¹² favorable absorption coefficient,²¹ and better carrier lifetime.²⁴ The development of SnO₂, SnBr₂, and SnBr₄ can lead to defects in CsSnBr₃ perovskite, which raises serious concerns about the stability of Sn-based PSCs. These defects, whether originating during material synthesis or due to exposure to moisture and oxygen, negatively impact both performance and stability.²⁵ To tackle these challenges, researchers have implemented various strategies. Hartmann et al. investigated the chemical and electronic structures of CsSnBr₃ and examined the impact of adding 20 mol % SnF₂ to the precursor solution. The results demonstrated that the addition of SnF₂ improves substrate coverage, reduces Sn^{II} to Sn^{IV} oxidation, and inhibits the formation of secondary Br and Cs species.²⁶ Despite these efforts, the efficiency remains considerably below the theoretical limit, indicating ample room for enhancing device performance and film stability. In 2015, Sabba et al. showcased an effective approach to elevate the V_{oc} of Sn-based perovskite by introducing Br anions into CsSnI_{3-x}Br_x. This incorporation induces a crystal structure shift from orthorhombic to cubic, transitioning from CsSnI₃ to CsSnBr₃. The modulation of the bandgap through Br-substitution is expected to augment the V_{oc} of CsSnI₃ perovskite without the need for an additional reducing agent like SnF₂. Device structures FTO/TiO₂/CsSnBr₃/Spiro-OMeTAD/Au and FTO/TiO₂/CsSnBr₃:SnF₂/Spiro-OMeTAD/Au demonstrated PCEs of 0.1 and 0.95%, respectively.²⁷ Song et al. introduced a novel process utilizing a reducing vapor atmosphere during cell preparation with CsSnBr₃ as the absorber, yielding significantly improved solar cells with an efficiency of 3.04%. The process reduces Sn⁴⁺/Sn²⁺ ratios by over 20%, mitigating carrier recombination and advancing the development of low-cost, Pb-free Sn-based halide PSCs. However, the inclusion of an organic hole transport layer (HTL) poses a challenge to the long-term operational stability.²⁸ Gupta et al. conducted a study focusing on the influence of SnF₂ in improving the properties of CsSnBr₃, resulting in a maximum PCE of 2.1%.¹¹ Gao et al. reported the synthesis of CsSnBr₃ perovskite using a solid-phase reaction. The CsSnBr₃ film created through this process exhibited stability in ambient air for 8 days. The novel device structure (FTO/TiO₂/CsSnBr₃/MoO₃/Al) demonstrated a PCE of 1.5%.²¹ In a study by Kumar et al., the integration of rGO (reduced graphene oxide) nanocomposite was showcased to improve the structural and optical characteristics of pure

CsSnBr₃. The most favorable SC outcomes were observed with a 3% rGO concentration, yielding key parameters of PCE = 5.27%, V_{oc} = 0.714 V, J_{sc} = 12.04 mA/cm², and an FF = 61.32%.²⁹ Nasrin et al. reported the PSC-based structure, FTO/TiO₂/CsSnBr₃/P3HT/Au, using SCAPS-1D. This device structure demonstrated PCE = 17.94%, V_{oc} = 1.3 V, J_{sc} = 18 mA/cm², and FF = 74.5%.²⁴ While Sn-based PSCs have seen rapid experimental progress, there is still untapped potential for improvement by optimizing different parameters and device configurations. This optimization not only provides insights for future experimental enhancements but also serves as a guide. In this investigation, we conducted simulations for a total of 70 (Figure S1) unique combinations [10 HTLs (Table S1) and 7 electron transport layers (ETLs)] of PSC device structures. Subsequently, we identified six optimal devices distinguished by exceptional characteristics and promising performance parameters. This study further delves into the optimization of the performance of these top six structures. This involves examining various optoelectronic properties and assessing the impact of absorber layer, ETL, and HTL thicknesses and interface properties. Additionally, we explore the effects of capacitance, M–S, J – V , and QE characteristics. The performance of the top devices is verified using the wxAMPS simulation tool, and a comparative analysis of SCAPS-1D results with published reports is conducted. By employing a variety of analytical techniques and theoretical simulations, an optimized configuration is developed, leading to the achievement of the highest reported efficiency to date at 22.43% using any simulation method for indium-doped tin oxide (ITO)/TiO₂/CsSnBr₃/Cu₂O/Au configuration.

2. NUMERICAL MODELING AND MATERIAL PARAMETERS

2.1. SCAPS-1D Numerical Simulation. SCAPS-1D is a semiconductor device modeling framework developed by the University of Gent to model and simulate PSCs.³⁰ The primary equations used in SCAPS-1D are Poisson equation (eq 1) as well as the continuity equations for both holes and electrons (eqs 2–3), given as follows

$$\frac{dE}{dx} = -\frac{d^2\varphi}{dx^2} = \frac{q}{\epsilon_0\epsilon_r} [p(x) - n(x) + N_D^+(x) - N_A^-(x) + \rho_i(x) - n_t(x)] \quad (1)$$

$$\frac{dp_n}{dt} = G_p - \frac{p_n - p_{n0}}{\tau_p} + p_n\mu_p \frac{dE}{dx} + \mu_p E \frac{dp_n}{dx} + D_p \frac{d^2p_n}{dx^2} \quad (2)$$

$$\frac{dn_p}{dt} = G_n - \frac{n_p - n_{p0}}{\tau_n} + n_p\mu_n \frac{dE}{dx} + \mu_n E \frac{dn_p}{dx} + D_n \frac{d^2n_p}{dx^2} \quad (3)$$

where q represents the electron charge, ϵ represents the dielectric permittivity, $G_{n/p}$ represents the electron (hole) generation rate, $D_{n/p}$ represents the electron (hole) diffusion coefficients, φ represents the electrostatic potential, and E represents the electric field.

Carrier transport occurs through an efficient interplay of diffusion and drift mechanisms, which can be elucidated in eqs 4 and 5

$$J_n(x) = qn\mu_n E + qD_n \frac{dn}{dx} = n\mu_n \frac{dE_{Fn}}{dx} \quad (4)$$

$$J_p(x) = qp\mu_p E - qD_p \frac{dp}{dx} = p\mu_p \frac{dE_{Fp}}{dx} \quad (5)$$

where $\mu_{n/p}$ represents the electron (hole) mobilities and $E_{Fn/Fp}$ is the quasi-Fermi level for electrons (holes).

2.2. wxAMPS Numerical Simulation. In this study, the wxAMPS, 1D SC simulation framework developed by the University of Illinois is used to perform a similar numerical investigation as a supplementary validation of the performance of PSC. In the wxAMPS framework, the 1D Poisson's equation is (6)

$$\frac{d}{dx} \left(-\varepsilon(x) \frac{d\phi}{dx} \right) = q[p(x) - n(x) + N_D^+(x) - N_A^-(x) + p_t(x) - n_t(x)] \quad (6)$$

where n (p) represents free electrons (holes), n_t (p_t) represents trapped electrons (holes), and N_D^+ (N_A^-) represents ionized donor (acceptor) dopants.

The continuity equation for both free electrons and holes, which describes how the PSC behaves in the various regions of the structure, is as follows (7 and 8)

$$\frac{1}{q} \frac{dJ_n}{dx} = -G_{op}(x) + R(x) \quad (7)$$

$$\frac{1}{q} \frac{dJ_p}{dx} = G_{op}(x) - R(x) \quad (8)$$

where J_n (J_p) represents the electron (hole) current densities, $R(x)$ represents the net rate of recombination, and $G_{op}(x)$ is a function of x , representing the rate of optical generation due to light supplied externally.

2.3. CsSnBr₃ PSC Structure. In the majority of perovskite device structures, the light harvesting layer (absorber) is placed between an ETL and HTL. Both these transport layers play a vital role in averting the recombination of photogenerated carriers and enhancing the charge transportation by establishing paths for the separation of carriers, allowing electrons and holes to move from the absorber layer to the electrodes and ultimately reach the external circuit through metal (Au) contacts.

In the present study, a normal (n-i-p) structure for the CsSnBr₃-based PSC (Figure 1) with ITO as the front contact, Cu₂O as the HTL, and gold (Au) as the back contact is employed. The charge transport layers TiO₂, PCBM, WS₂, SnO₂, ZnO, and IGZO are used as ETLs. Figure 1b represents the structure of the optimized CsSnBr₃-based SC. Figure 1c demonstrates the energy band diagram of the CsSnBr₃-based SC where WS₂ is used as the ETL along with Cu₂O HTL, and Figure 1d represents the corresponding energy band alignment.

The input simulation parameters for ETL, CsSnBr₃ absorber layer, and HTL required for SCAPS-1D simulation are extracted from relevant literature sources, which are detailed in Tables 1 and S1. Table 2 details the simulation parameters related to interface defect density. The energy band alignment between ITO, different ETLs (TiO₂, PCBM, WS₂, SnO₂, IGZO, and C₆₀), HTL, and back metal contact (Au) is demonstrated in Figure 2. All the simulations were conducted

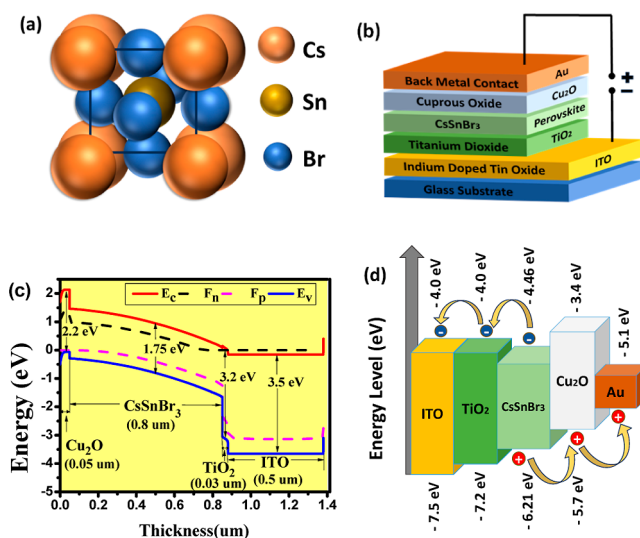


Figure 1. (a) Crystal structure of the CsSnBr₃ absorber, (b) PSC device architecture, (c) energy band diagram, and (d) energy band alignment of an optimized PSC.

under standard conditions, which include a frequency of 1 MHz and exposure to the AM 1.5G spectrum of sunlight at an ambient temperature of 300 K.

3. RESULTS AND DISCUSSION

3.1. Verification of the Device Models. To validate the material parameters, our initial emphasis was placed on the examination of a device incorporating TiO₂ as the ETL, CsSnBr₃ as the light absorption layer, and Spiro-OMeTAD as the HTL. The energy band diagram of the considered device is presented in Figure 3a, and the simulated J - V curves are illustrated in Figure 3b. The device exhibits a V_{oc} of 0.85 V, J_{sc} of 20.63 mA cm⁻², FF of 59.6%, and PCE of 10.45%.

These results align with the experimental findings reported by Chen et al.,³³ thereby reinforcing the integrity of the numerical simulation. As a result, the chosen material parameters for the device model are justified, providing a solid foundation to proceed with subsequent experiments centered around this specific device setup. A minor disparity between the experimental outcomes and our computed results lies in the utilization of ITO and Au layers as front and back electrodes in our present research. Notably, the thickness of these front and back contacts remains fixed. Conversely, in the experimental investigation, these layers were used with specifically tailored thicknesses to suit the device configuration.

3.2. Effect of the Absorber Layer Thickness and Defect Density on PV Performance. The optimized thickness of the absorber layer as well as defect density (N_t) play an important role in determining the performance of the SC. Figures 4a-f, 5a-f, 6a-f, and 7a-f show the impact of absorber layer thickness and N_t through contour plots on the PCE, V_{oc} , J_{sc} , and FF, respectively, for the structures under investigation. The absorber layer thickness is varied from 200 to 1200 nm, and N_t is adjusted from 1×10^{13} to 1×10^{20} cm⁻³ to determine the optimal absorber defect density and thickness.

In contour plots 4a-f, all the six optimized ETL-based structures exhibit a consistent trend: as the absorber thickness increases, the PCE rises, while a simultaneous increase in defect density results in a decrease in PCE. Contour plots

Table 1. Initial Input Simulation Parameters for ITO, ETLs, HTL, and CsSnBr₃ Absorber Layer^{24,31,32}

parameters	ITO	TiO ₂	PCBM	WS ₂	SnO ₂	ZnO	IGZO	Cu ₂ O	CsSnBr ₃
thickness (nm)	500	30	50	100	100	50	30	50	800 ^a
E_g (eV)	3.5	3.2	2	1.8	3.6	3.3	3.05	2.2	1.75
χ (eV)	4	4	3.9	3.95	4	4	4.16	3.4	4.07
ϵ_r	9	9	3.9	13.6	9	9	10	7.5	5.9
N_C (1/cm ³)	2.2×10^{18}	2×10^{18}	2.5×10^{21}	1×10^{18}	2.2×10^{18}	3.7×10^{18}	5×10^{18}	2×10^{19}	1×10^{19}
N_V (1/cm ³)	1.8×10^{19}	1.8×10^{19}	2.5×10^{21}	2.4×10^{19}	1.8×10^{19}	1.8×10^{19}	5×10^{18}	1×10^{19}	1×10^{19}
μ_n (cm ² V ⁻¹ s ⁻¹)	20	20	0.2	100	100	100	15	200	2
μ_p (cm ² V ⁻¹ s ⁻¹)	10	10	0.2	100	25	25	0.1	8600	2
N_A (1/cm ³)	0	0	0	0	0	0	0	1×10^{18}	1×10^{15a}
N_D (1/cm ³)	1×10^{21}	9×10^{16}	2.93×10^{17}	1×10^{18}	1×10^{17}	1×10^{18}	1×10^{17}	0	0
N_t (1/cm ³)	1×10^{15a}	1×10^{15a}	1×10^{15a}	1×10^{15a}	1×10^{15a}	1×10^{15a}	1×10^{15a}	1×10^{15a}	1×10^{15a}

^aIn this study, these parameters are kept constant during the first optimization to obtain an optimal ETL and HTL.

Table 2. Input Parameters of Interface Defect Layers^{31,32}

interface	defect type	energetic distribution	capture cross section: electrons/holes (cm ²)	reference for defect energy level	energy with respect to reference (eV)	total density (cm ⁻³) (integrated over all energies)
ETL/CsSnBr ₃	neutral	single	1.0×10^{-17}	above the VB maximum	0.6	1.0×10^{10}
HTL/CsSnBr ₃	neutral	single	1.0×10^{-18}	above the VB maximum	0.6	1.0×10^{10}

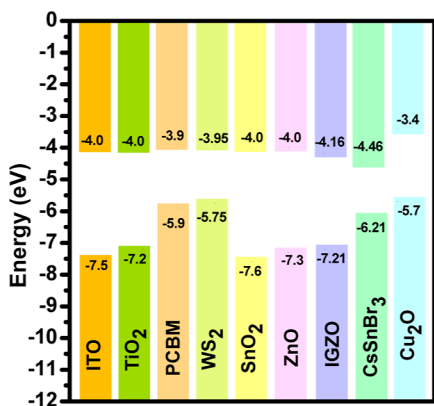


Figure 2. Energy band alignment between ITO, different ETLs (TiO₂, PCBM, WS₂, SnO₂, ZnO, and IGZO), HTL (Cu₂O), and back metal contact (Au).

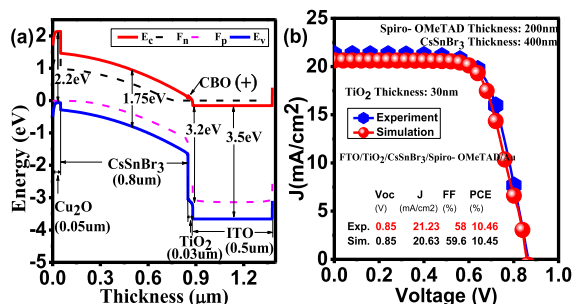


Figure 3. (a) Energy band schematic and (b) experimental and modeled J - V curves of the ITO/TiO₂/CsSnBr₃/Spiro-OMeTAD/Au PSCs.

reveal that PCE exhibits a strong sensitivity to changes in N_t . As N_t increases from 1×10^{13} to 1×10^{14} cm⁻³ across all device structures, PCE initiates a decline, and as N_t continues to increase, PCE deteriorates further. TiO₂-, PCBM-, WS₂-,

SnO₂-, ZnO-, and IGZO-based PSCs demonstrate the highest PCE of 23.94, 23.80, 23.98, 23.93, 23.90, and 23.94%, respectively, with absorber thickness set at 1200 nm and N_t set at 1×10^{13} cm⁻³. The increase in N_t significantly impairs the PCE in all the six optimized PSC-based structures. For TiO₂-, PCBM-, WS₂-, SnO₂-, ZnO-, and IGZO-based PSC structures, PCE reduces from 23.93 to 0.014%, 23.8 to 0.012%, 23.98 to 0.008%, 23.93 to 0.014%, 23.94 to 0.013%, and 23.90 to 0.016%, respectively, as N_t increases from 1×10^{14} to 1×10^{20} cm⁻³ with an absorber length of 1200 nm. The contour plots clearly demonstrate that decreasing the thickness of the absorber leads to a drop in the PCE. This phenomenon arises due to the utilization of thinner absorber layers, which allows a significant portion of light (photons) to traverse the material without being absorbed. As a result, there is a decrease in the generation of electron-hole (e-h) pairs, leading to a subsequent fall in the PCE. In contrast, as the thickness of the absorber grows, there is a corresponding increase in optical absorption and the formation of e-h pairs, resulting in an improved PCE.³⁴ Nevertheless, once the absorber thickness exceeds the optimal value, PCE begins to decrease. This loss is attributed to increased recombination caused by a longer diffusion length.^{35,36} Defect density in PSCs hinders the SC performance by acting as sites where carriers recombine and as pathways for ion migration when exposed to light.³⁷

In contour plots 5a-f, the V_{oc} for various ETL-based structures is depicted. It is evident that an increase in both the absorber thickness and N_t leads to a reduction in V_{oc} . When N_t increases from 1×10^{13} to 1×10^{14} cm⁻³ for all device structures, V_{oc} begins to decrease, and further increases in N_t worsen V_{oc} . Among the PSCs studied, TiO₂-, WS₂-, SnO₂-, ZnO-, and IGZO-based structures exhibit the highest V_{oc} values, measuring 1.465, 1.471, 1.465, 1.466, and 1.454 V, respectively. Conversely, the PCBM-based structure shows the lowest V_{oc} of 1.381 V, given an absorber thickness of 100 nm and N_t set at 1×10^{13} cm⁻³. Increasing N_t and absorber thickness significantly reduces V_{oc} in all the six optimized PSC-

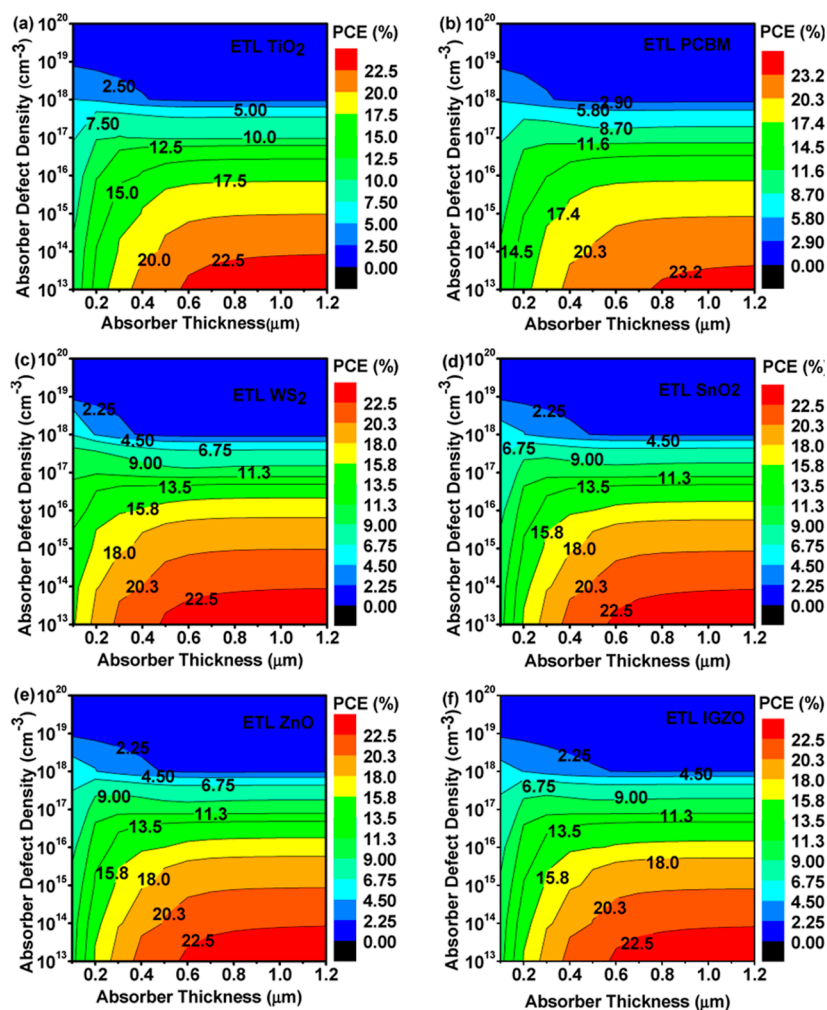


Figure 4. Contour plots depict the influence of absorber defect density and absorber thickness on PCEs for different ETLs.

based structures. For TiO_2 -, PCBM -, WS_2 -, SnO_2 -, ZnO -, and IGZO -based PSC structures, V_{oc} decreases from their respective initial values to ~ 0.697 V as N_t increases from 1×10^{13} to $1 \times 10^{20} \text{ cm}^{-3}$ with an absorber thickness of 1000 nm.

In contour plots 6a–f, J_{sc} values for various ETL-based structures are displayed. It is clear that an increase in N_t leads to a reduction in J_{sc} . The contour plots further reveal that as the absorber layer thickness is increased from 100 to 1200 nm and N_t is simultaneously raised from 1×10^{13} to $1 \times 10^{17} \text{ cm}^{-3}$, there is no significant decrease in J_{sc} . However, once N_t surpasses $1 \times 10^{17} \text{ cm}^{-3}$, J_{sc} experiences a substantial drop, regardless of the absorber thickness. TiO_2 -, PCBM -, WS_2 -, SnO_2 -, ZnO -, and IGZO -based PSC structures exhibited the highest J_{sc} , reaching 19.8 mA/cm^2 when the absorber thickness was set at 1200 nm. This value decreased to 0.05 mA/cm^2 when N_t was increased to $1 \times 10^{20} \text{ cm}^{-3}$.

In contour plots 7a–f, FF values for various ETL-based structures are displayed. The contour plots provide an insight into how altering the absorber thickness along with N_t affects the FF of PSCs. When N_t is in the range of 1×10^{13} to $1 \times 10^{15} \text{ cm}^{-3}$, the FF decreases gradually. However, once N_t surpasses $1 \times 10^{15} \text{ cm}^{-3}$, there is a sudden and significant decline in the FF across all absorber layer thicknesses within the six optimized structures. Devices based on TiO_2 -, PCBM -, WS_2 -, SnO_2 -, ZnO -, and IGZO exhibited the highest FF values,

measuring 85.2, 88.99, 86.14, 85.25, 85.12, and 85.49%, respectively, for an absorber thickness of 100 nm and N_t set at $1 \times 10^{13} \text{ cm}^{-3}$. This value was reduced to approximately 38–36% from its initial value when the absorber thickness remained at 100 nm and N_t was raised to $1 \times 10^{20} \text{ cm}^{-3}$ for all device structures.

3.3. Effect of the Absorber Defect Density and Defect Energy Level on PV Performance. In this section, the influence of CsSnBr_3 defect density (N_t) and defect energy level (E_t) on the performance of PSC is investigated. The performance of PSCs is contingent upon two pivotal processes: the generation of charge carriers and their subsequent recombination. When PSCs are exposed to sunlight, the absorber layer triggers the creation of photoinduced carriers, encompassing both electrons and holes. These newly formed carriers are then channeled toward the electrode and directed into the external circuit. However, during the collection of these photoinduced charge carriers, a substantial portion is lost due to poor quality of the absorber layer. This diminished quality results in a higher defect density, which in turn accelerates the recombination rate within low-quality absorber films. This increased recombination rate not only curtails the distance over which charge carriers can travel (diffusion length) but also shortens their lifespan. To shed light on the underlying recombination mechanisms and how they are

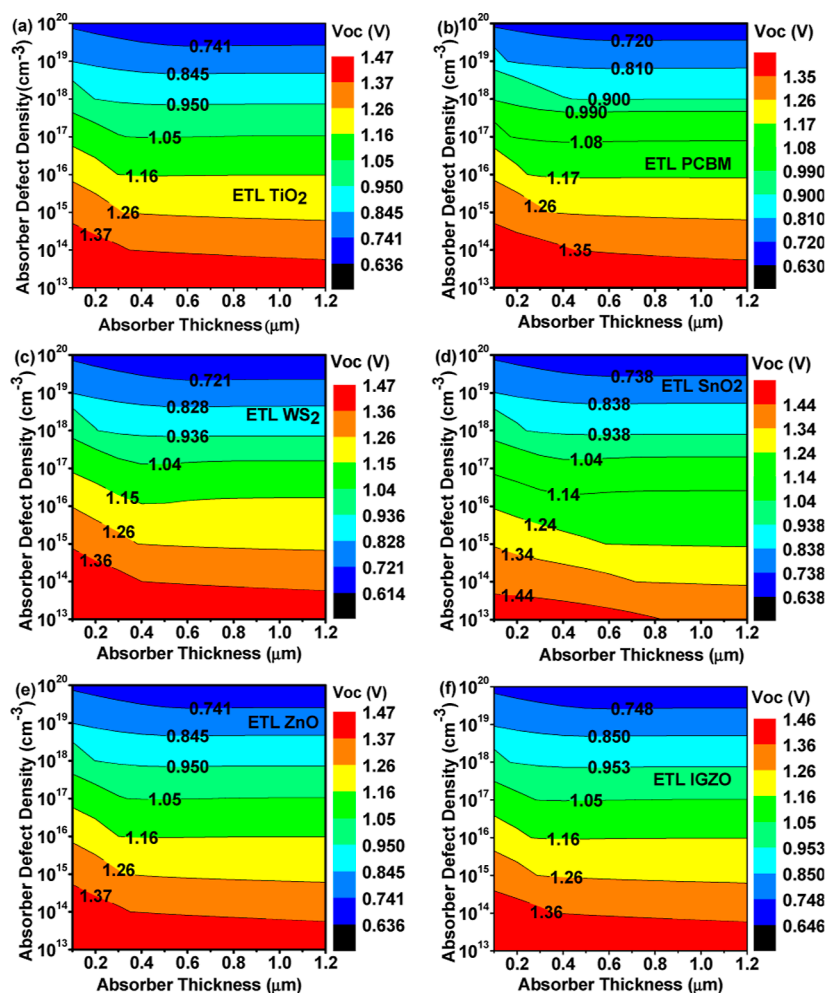


Figure 5. Contour plots depict the influence of absorber defect density and absorber thickness on V_{oc} for different ETLs.

influenced by defect density, eq 9 delineates the Shockley–Read–Hall (SRH) recombination process as³⁸

$$\mathcal{R}^{SRH} = \frac{V_t \sigma_n \sigma_p N_t [np - n_i^2]}{\sigma_p [p + p_1] + \sigma_n [n + n_1]} \quad (9)$$

where N_t is the defect density, τ_p (τ_n) defines the lifetime of holes (electrons), and σ_p (σ_n) specifies the capture cross section of holes (electrons).

Parameters p_1 and n_1 introduce the dependency of the recombination rate on the trapping energy level, E_v , in the following manner³⁹

$$p_1 = N_v e^{(E_v - E_t)/kT} \quad (10)$$

$$n_1 = N_c e^{(E_t - E_c)/kT} \quad (11)$$

where E_t is the trap energy level.

In eq 9, it is observed that the \mathcal{R}^{SRH} effect is directly linked to the N_t within the light harvesting layer. Haider and colleagues found that reducing N_t in the absorber layer results in an increase in diffusion length. This phenomenon, in turn, enhances the overall performance of PSCs.³⁹ To assess the impact of N_t on the device's performance, the carrier diffusion length (L_D) in eq 12 is considered, incorporating the SRH effect (\mathcal{R}^{SRH}) and carrier lifetime ($\tau_{(e,h)}$) as^{38,39}

$$L_D = \sqrt{\frac{\mu_{(e,h)} \mathcal{R}^{SRH} T \tau_{(e,h)}}{q}} \quad \text{and} \quad \tau_{(e,h)} = \frac{1}{N_t \sigma_{n,p} V_T} \quad (12)$$

Analyzing eqs 9–12, we can draw the conclusion that an increase in defect density increases the recombination rate, subsequently leading to a reduction in the diffusion length and carrier lifetime, thereby reducing the number of carriers reaching the contacts. Therefore, it becomes quite evident that as the value of N_t increases, the degradation in the overall performance of PSCs becomes more pronounced.

Figures 8a–f, 9a–f, 10a–f, and 11a–f demonstrate the contour plots of the important PV performance parameters: PCE, V_{oc} , J_{sc} , and FF, respectively, with the simultaneous variation of absorber defect density (N_t) and defect energy level (E_t) for different ETLs in our investigation. The N_t spans a range from 1×10^{14} to 1×10^{19} cm^{-3} , while the E_t extends from 0 to 1.2 eV. Analysis through contour plots 8a–f reveals a consistent trend: when simultaneously adjusting both N_t and E_t , the PCE decreases across all six ETL-based device structures. In the case of a TiO_2 -based device, the highest PCE achieved is 24.83%, occurring when E_t is 0 eV and N_t is 1×10^{14} cm^{-3} . However, as N_t increases to 1×10^{19} cm^{-3} while keeping E_t constant at 0 eV, the PCE drops to 20.36%. Furthermore, when N_t is raised to 1×10^{19} cm^{-3} and E_t is increased to 1.2 eV from its initial value, the PCE decreases to 21.76%. Additionally, a shift in E_t from 0 to 0.6 eV, with N_t

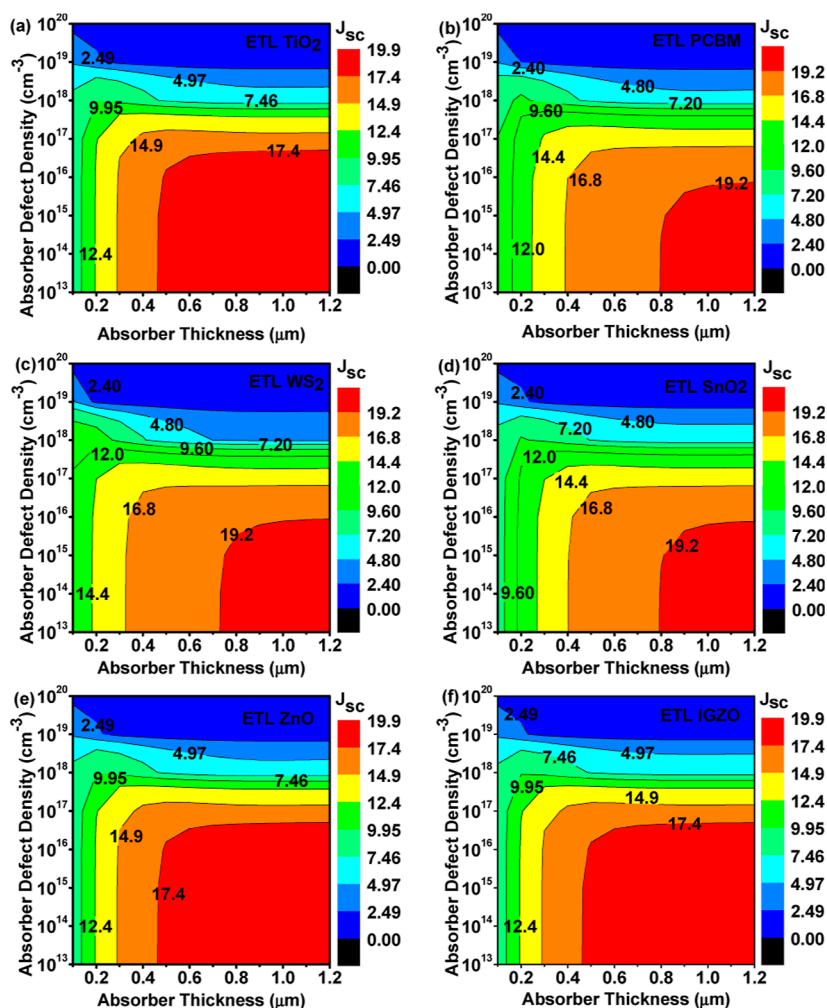


Figure 6. Contour plots depict the influence of absorber defect density and absorber thickness on J_{sc} for different ETLs.

exceeding $1 \times 10^{16} \text{ cm}^{-3}$, results in a significant decline in PCE. However, when E_t surpasses 0.6 eV, there is no substantial change in PCE for any value of N_t , as indicated by the contour plots. This consistent pattern is observed across all six PSC-based structures. For instance, in a TiO_2 -based structure, when E_t is at 0 eV and N_t is $1 \times 10^{14} \text{ cm}^{-3}$, the PCE reaches 24.83%. As E_t increases from 0 to 0.6 eV, the PCE decreases to 21.765%, and once the E_t surpasses 0.6 eV, the PCE remains relatively stable. With N_t of $1 \times 10^{19} \text{ cm}^{-3}$ and an E_t of 1.2 eV, the PCE drops to 0.17%. In the case of PCBM-, WS_2 -, SnO_2 -, ZnO -, and IGZO-based devices, the highest PCE achieved is 24.13, 24.99, 24.82, 24.83, and 24.79%, respectively, when E_t is set at 0 eV and N_t at $1 \times 10^{14} \text{ cm}^{-3}$.

Contour plots 9a–f provide insights into how an increase in N_t and E_t results in a decrease in V_{oc} for all the six optimized structures. In devices based on TiO_2 , PCBM, WS_2 , SnO_2 , ZnO , and IGZO, the highest V_{oc} values achieved are 1.50, 1.40, 1.49, 1.50, 1.50, and 1.49 V, respectively, when N_t is set at $1 \times 10^{14} \text{ cm}^{-3}$ and E_t is at 0 eV. However, when N_t is increased to $1 \times 10^{19} \text{ cm}^{-3}$ and E_t is raised to 1.2 eV, V_{oc} decreases to 0.764, 0.757, 0.740, 0.764, 0.763, and 0.77 V, respectively, from their initial values. However, when E_t surpasses 0.6 eV, there is no substantial change in V_{oc} for any value of N_t , as indicated by the contour plots. This consistent pattern is observed across all six PSC-based structures. A study by Soucase et al. highlights the significant influence of the N_t on V_{oc} .⁴⁰ From a practical

standpoint, the N_t in the absorber layer depends on factors such as formation energy and chemical potential, which, in turn, are influenced by precursors, partial pressure, and synthesis temperature. Defects with low formation energy only create shallow levels, leading to longer carrier diffusion lengths (L_D) and higher V_{oc} . Conversely, defects with deep energy levels have higher formation energy, adversely affecting L_D and V_{oc} . Experimental and simulation results confirm the existence of unusually shallow defect levels, which are associated with long L_D and high V_{oc} . The quality of the absorber layer determines the L_D , and if L_D exceeds the thickness of the absorber layer, it results in superior device performance. The relationship between L_D , J_o , and V_{oc} can be summarized as follows⁴¹

$$J_o \approx q \frac{Dn_i^2}{L_D N} \quad (13)$$

$$V_{oc} = \frac{KT}{q} \ln \left(\frac{J_{sc}}{J_o} + 1 \right) \quad (14)$$

where J_o is the recombination current.

Based on eqs 13 and 14, it can be inferred that reducing N_t leads to a decrease in J_o , which enhances L_D and, consequently improving V_{oc} , aligning with the results obtained.

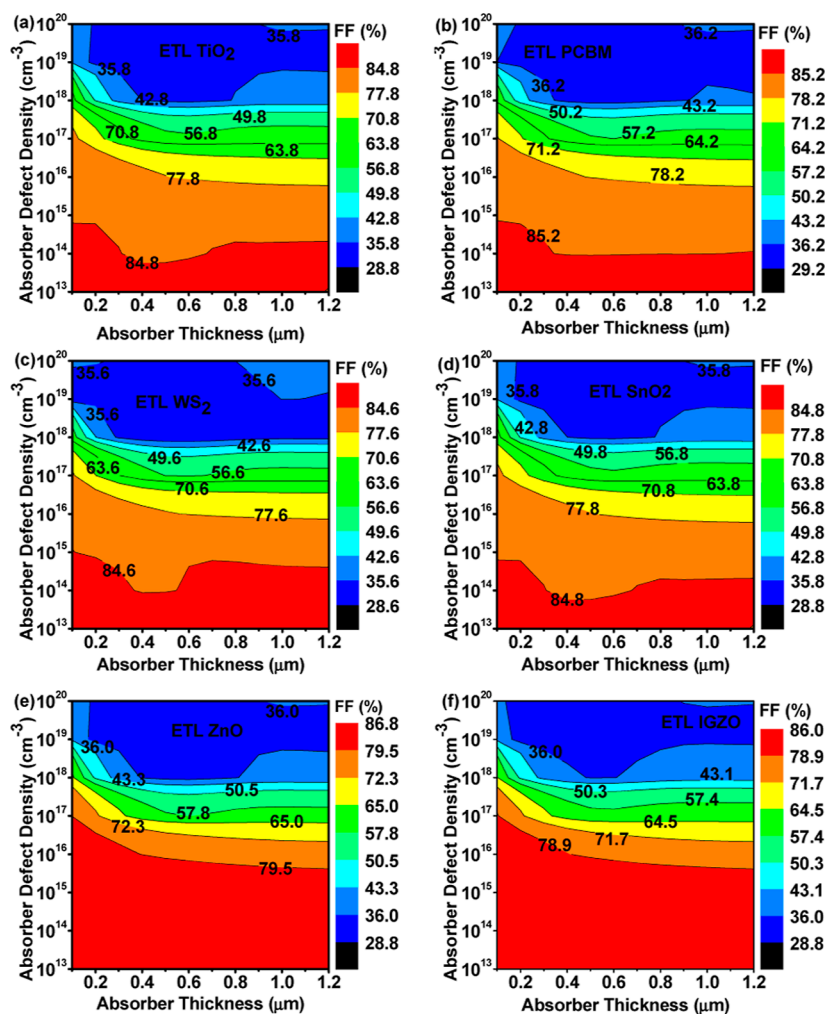


Figure 7. Contour plots depict the influence of absorber defect density and absorber thickness on FF for different ETLs.

Contour plots 10a–f illustrate a significant reduction in J_{sc} values when both N_t and E_t of the absorber layer vary simultaneously. In the case of devices utilizing TiO_2 , PCBM, WS_2 , SnO_2 , ZnO, and IGZO, the highest J_{sc} values obtained are 19.23, 19.21, 19.37, 19.22, 19.22, and 19.22 mA/cm^2 , respectively, when N_t is set at $1 \times 10^{14} cm^{-3}$ and E_t is at 0 eV. However, when N_t is increased to $1 \times 10^{19} cm^{-3}$ and E_t is raised to 1.2 eV, J_{sc} decreases to 0.66, 0.57, 0.40, 0.67, 0.65, and 0.77 mA/cm^2 , respectively, from their initial values. Nevertheless, when E_t exceeds 0.6 eV, J_{sc} remains relatively constant for any value of N_t , as indicated by the contour plots across all the six PSC-based structures.

Contour plots 11a–f demonstrate a significant reduction in FF values when both N_t and E_t of the absorber layer vary simultaneously. In the case of devices utilizing TiO_2 , PCBM, WS_2 , SnO_2 , ZnO, and IGZO, the highest FF values obtained are 85.88, 89.59, 86.6, 85.9, 85.87, and 86.24%, respectively, when N_t is set at $1 \times 10^{14} cm^{-3}$ and E_t is at 0 eV. However, when N_t is increased to $1 \times 10^{19} cm^{-3}$ and E_t is raised to 1.2 eV, FF decreases to 32.68, 33.23, 33.91, 32.66, 32.68, and 33.37%, respectively, from their initial values. Nevertheless, when E_t exceeds 0.6 eV, FF remains relatively constant for any value of N_t , as indicated by the contour plots across all the six PSC-based structures.

3.4. Influence of Layer Thickness on PV Performance.

3.4.1. Influence of $CsSnBr_3$ Layer Thickness on PV Perform-

ance. The diffusion length and lifetime of the photogenerated carriers are significantly affected by the thickness of the absorber layer. To optimize the performance of six device structures, the thickness of $CsSnBr_3$ varies from 100 to 1200 nm, as illustrated in Figure 12. As the absorber thickness increases, both the PCE and J_{sc} experience rapid growth until reaching 800 nm. Beyond this point, there is no significant change, indicating saturation in the number of photons available for absorption. Conversely, V_{oc} and FF exhibit a monotonic decrease with the increase in $CsSnBr_3$ layer thickness. This reduction in V_{oc} and FF is attributed to the rise in recombination rate and improved series resistance, respectively. Among the device structures based on TiO_2 , PCBM, WS_2 , SnO_2 , ZnO, and IGZO, the highest PCEs are observed at 19.72, 19.73, 20.02, 19.72, 19.73, and 19.57%, respectively, with an optimal absorber thickness of 800 nm. In the ITO/ TiO_2 / $CsSnBr_3$ / Cu_2O /Au device, an increase in absorber thickness resulted in a decrease in V_{oc} , reducing from 1.33 to 1.22 V. Additionally, J_{sc} exhibited an initial rise from 8.37 to 19.17 mA/cm^2 , followed by a slight increase to 19.73 mA/cm^2 . Similarly, the absorber thickness variation led to an initial increase in FF from 83.48 to 84.16%, succeeded by a decrease to 82.37%. Furthermore, PCE demonstrated an increase from 9.27 to 19.72%, with a subsequent further rise to 19.87%. Likewise, devices ITO/PCBM/ $CsSnBr_3$ / Cu_2O /Au, ITO/ WS_2 / $CsSnBr_3$ / Cu_2O /Au, ITO/ SnO_2 / $CsSnBr_3$ / Cu_2O /

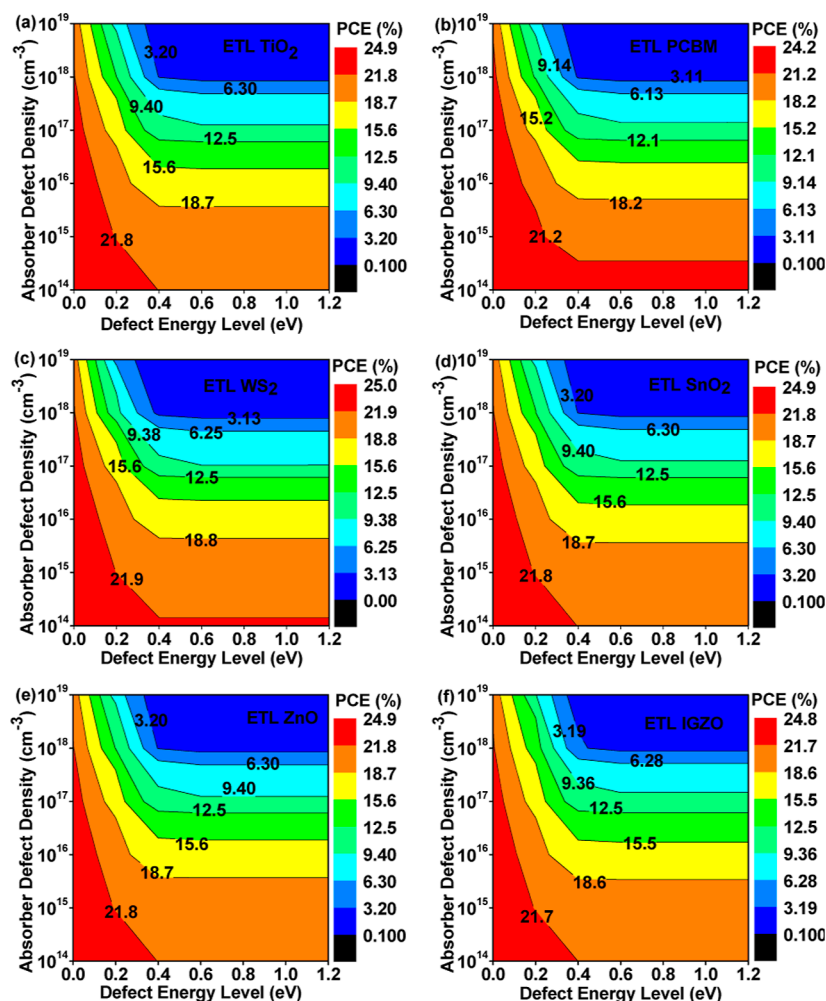


Figure 8. Contour plots depict the influence of defect energy level and absorber defect density on PCEs for different ETLs.

Au, ITO/ZnO/CsSnBr₃/Cu₂O/Au, and ITO/IGZO/CsSnBr₃/Cu₂O/Au displayed similar trends in PV parameters with an increase in absorber thickness. Since the highest efficiency was achieved at 800 nm, this thickness was deemed optimal for the simulated PSC structures.

3.4.2. Influence of ETL Thickness on PV Performance. Figure 13 illustrates the impact on photovoltaic parameters as the thickness of the ETL varies from 50 to 600 nm, while maintaining constant values for other parameters and setting the absorber thickness at 800 nm. In Figure 13a, it is apparent that an increase in ETL thickness leads to a marginal improvement in PCE for all device structures, except for the PCBM-based PSC, where PCE sharply declines from 19.72 to 9.81%. In Figure 13b, the V_{oc} for all devices remains stable with an increase in ETL thickness, except for the PCBM-based PSC, where V_{oc} decreases from 1.229 to 1.204 V. This trend continues in Figure 13c, where a slight rise in J_{sc} is observed with an increase in ETL thickness. For PCBM- and WS₂-based devices, J_{sc} increases from 19.16 to 19.18 mA/cm² and 19.32 to 19.33 mA/cm², respectively. In TiO₂-, SnO₂-, ZnO-, and IGZO-based devices, J_{sc} increases from 19.17 to 19.19 mA/cm². Figure 13d demonstrates that, with an increase in ETL thickness, TiO₂-, SnO₂-, and ZnO-based devices exhibit negligible changes in FF, while the PCBM-based device sees an increase up to 200 nm, followed by a decrease. WS₂ displays a continuous gradual increase in FF, whereas IGZO shows a

continuous gradual decrease in FF with increasing ETL thickness. The optimized thicknesses for TiO₂, PCBM, WS₂, SnO₂, ZnO, and IGZO are determined to be 300, 100, 500, 200, 200, and 200 nm, respectively.

3.4.3. Influence of HTL Thickness on PV Performance. Figure 14 illustrates the impact of varying the thickness of the Cu₂O HTL on PV parameters in CsSnBr₃-based PSCs, utilizing TiO₂, PCBM, WS₂, ZnO, SnO₂, and IGZO as ETLs.

Adjusting the HTL thickness is crucial for optimizing performance while minimizing direct contact between the absorber layer and the cathode. Additionally, this layer serves the dual purpose of acting as a capping layer. The focus of our investigation for HTL thickness optimization is solely on Cu₂O since among all HTLs given in Table S1, it demonstrated the highest PCE when used with six different ETLs. Figure 14 reveals that the values of PCE, V_{oc} , J_{sc} , and FF remain consistent for all ETLs as the thickness of Cu₂O (HTL) increases. V_{oc} remains stable at approximately 1.22 V for TiO₂, PCBM, SnO₂, ZnO, and IGZO as ETLs and at around 1.23 V for WS₂ with increased Cu₂O thickness. The J_{sc} value for PCBM shows a lower value of 19.16 mA/cm², while WS₂ demonstrates a higher value of 19.32 mA/cm² with an increasing Cu₂O thickness. Among all ETLs, WS₂ achieves the highest FF and PCE values at around 84.18 and 20.02%, respectively. On the other hand, the IGZO ETL exhibits the lowest PCE value of about 19.57% with increasing Cu₂O

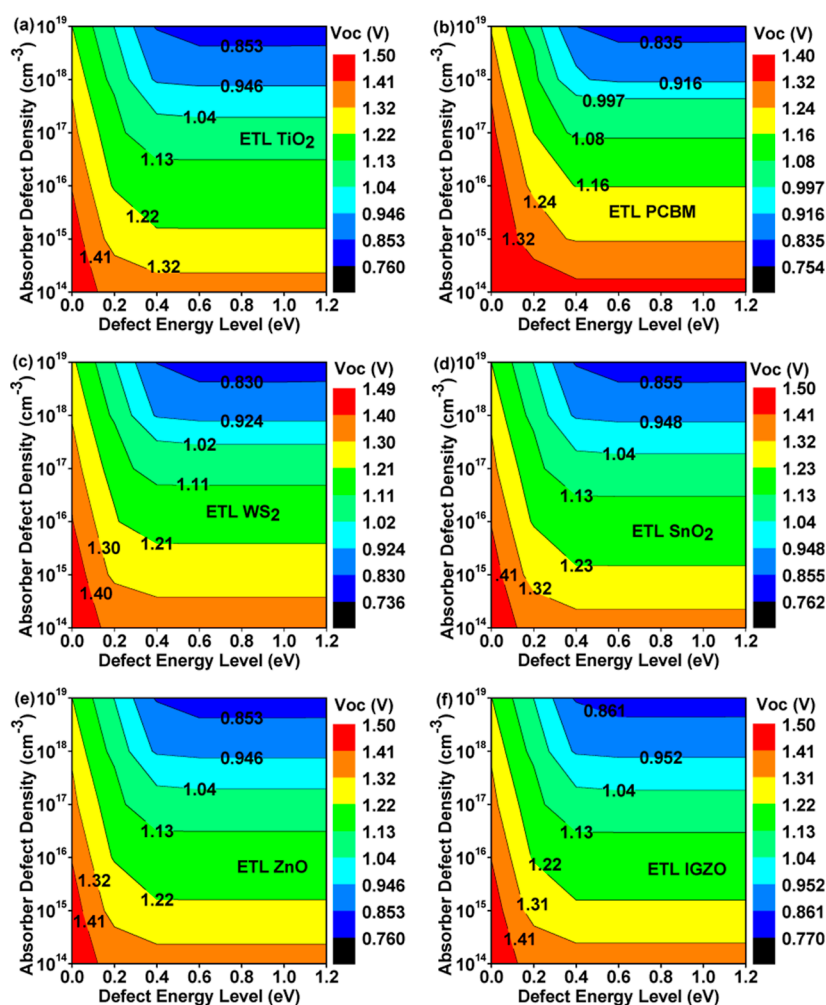


Figure 9. Contour plots depict the influence of defect energy level and absorber defect density on V_{oc} for different ETLs.

thickness. Previous studies have indicated an increase in PCE with an increase in HTL thickness. Typically, a thicker p-type layer is preferred over an n-type layer to reduce the recombination rate, facilitating efficient charge carrier transportation to the respective terminals. However, during the variation of HTL thickness, it is observed that 50 nm is the optimal thickness for achieving higher PCE. Therefore, 50 nm is selected as the optimized HTL thickness for subsequent calculations.

3.5. Influence of the PAL Properties. **3.5.1. Influence of the PAL Acceptor Doping Density.** The acceptor density (N_A) within the perovskite absorber layer (PAL) is a critical parameter influencing PSC performance. Elevating N_A can enhance V_{oc} and efficiency; however, excessive levels may lead to increased recombination and an imbalanced charge distribution. Therefore, finding the optimal balance between N_A and other factors is crucial for achieving maximum efficiency and stability. Additionally, the PSC performance is influenced by the absorbers' electrical behavior, which is itself affected by N_A . N_A was systematically varied from 1×10^{10} to $1 \times 10^{21} \text{ cm}^{-3}$ to assess its impact on the PV parameters of the device and determine the optimal N_A for the absorber. Figure 15 depicts the changes in PCE, V_{oc} , J_{sc} , and FF with varying N_A in the absorber for devices with different ETLs. For the ITO/TiO₂/CsSnBr₃/Cu₂O/Au device, as shown in Figure 15a, when $1 \times 10^{10} \text{ cm}^{-3} < N_A < 1 \times 10^{14} \text{ cm}^{-3}$, there is no

significant increase in PCE. However, for $N_A > 1 \times 10^{14} \text{ cm}^{-3}$, PCE increased from 18.89 to 22.73%. Figure 15b,c exhibits a similar trend, with no substantial changes in V_{oc} and J_{sc} as N_A is varied from 1×10^{10} to $1 \times 10^{14} \text{ cm}^{-3}$. When $N_A > 1 \times 10^{14} \text{ cm}^{-3}$, V_{oc} increased from 1.22 to 1.57 V, and J_{sc} decreased from 19.17 to 15.683 mA/cm².

Figure 15d indicates that as N_A ranged from 1×10^{10} to $1 \times 10^{15} \text{ cm}^{-3}$, FF increased from 79.8 to 83.66%. For $N_A = 1 \times 10^{16} \text{ cm}^{-3}$, FF dropped to 82.45%, but further increases in N_A resulted in elevated FF. Similar performance trends were observed in all other structures. Considering the pursuit of maximum efficiency, the optimum N_A in the PAL was determined as $1 \times 10^{15} \text{ cm}^{-3}$ for all ETLs.

3.5.2. Influence of the PAL Defect Density. The defect density (N_t) within the PAL plays a crucial role in influencing the performance of a PSC. Various types of defects in the perovskite material, such as lattice, chemical, or electronic defects, can act as recombination centers for charge carriers, thereby diminishing the overall efficiency of the SC. An increase in N_t within the PAL results in reduced efficiency as higher N_t introduces more recombination centers for charge carriers, leading to a decline in the overall current generated by the cell. Additionally, defects have the capacity to trap charge carriers, thereby reducing the charge carrier lifetime and, consequently, efficiency. Another significant factor impacting the PSC's performance is the long-term stability of the SC.

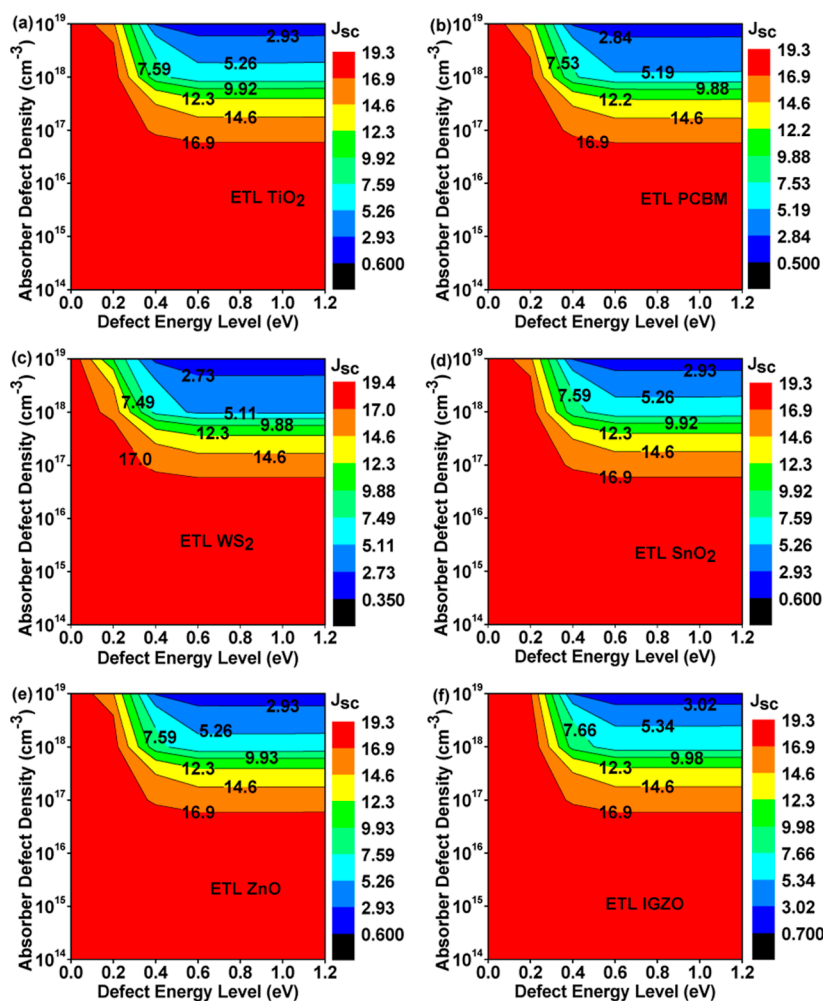


Figure 10. Contour plots depict the influence of defect energy level and absorber defect density on J_{sc} for different ETLs.

Defects can contribute to the instability of the perovskite layer, making it more susceptible to degradation under operational conditions. Therefore, this subsection delves into studying the impact of defect density to provide deeper insights. In Figure 16, the relationship between N_t and PV parameters is illustrated as N_t varies from 1×10^{10} to $1 \times 10^{20} \text{ cm}^{-3}$. Notably, there is no significant change in PV parameters until N_t of the absorber reaches 10^{13} cm^{-3} . Beyond this point, PV parameters show a tendency to decrease from $1 \times 10^{14} \text{ cm}^{-3}$ onward. This decline in performance is attributed to SRH nonradiative recombination, defined in eqs 9 and 12, which is a key contributor to device lifetime reduction and carrier recombination.

Figure 16a shows that TiO_2 -, PCBM -, WS_2 -, SnO_2 -, ZnO -, and IGZO -based devices experience a reduction in PCE from 24.84 to 0.013%, 24.13 to 0.011%, 25.01 to 0.008%, 24.84 to 0.013%, 24.84 to 0.013%, and 24.81 to 0.016%, respectively, within the N_t range of 1×10^{10} to $1 \times 10^{20} \text{ cm}^{-3}$. Figure 16b demonstrates a consistent decrease in V_{oc} across all device structures with an increase in N_t . For TiO_2 -, PCBM -, WS_2 -, SnO_2 -, ZnO -, and IGZO -based devices, V_{oc} reduces from 1.54 to 0.63 V, 1.40 to 0.63 V, 1.50 to 0.61 V, 1.54 to 0.63 V, 1.54 to 0.63 V, and 1.51 to 0.64 V, respectively, over the N_t range from 1×10^{10} to $1 \times 10^{20} \text{ cm}^{-3}$. Figure 16c reveals that J_{sc} remains unchanged as $N_t \leq 1 \times 10^{14} \text{ cm}^{-3}$. However, once $N_t > 1 \times 10^{14} \text{ cm}^{-3}$, J_{sc} significantly decreases for all device

configurations. For TiO_2 -, PCBM -, WS_2 -, SnO_2 -, ZnO -, and IGZO -based devices, J_{sc} is reduced from 19.22 to 0.061 mA/cm^2 , 19.21 to 0.051 mA/cm^2 , 19.37 to 0.038 mA/cm^2 , 19.22 to 0.06 mA/cm^2 , 19.22 to 0.06 mA/cm^2 , and 19.22 to 0.07 mA/cm^2 , respectively, within the N_t range of 1×10^{10} to $1 \times 10^{20} \text{ cm}^{-3}$. Figure 16d depicts an increase in FF when $N_t \leq 1 \times 10^{14} \text{ cm}^{-3}$ for all structures. For TiO_2 -, PCBM -, WS_2 -, SnO_2 -, ZnO -, and IGZO -based devices, FF increases from 83.87 to 84.94%, 89.58 to 85.13%, 85.89 to 85.03%, 83.90 to 84.94%, 83.79 to 84.95%, and 85.02 to 84.85%, respectively. Beyond $N_t > 1 \times 10^{14} \text{ cm}^{-3}$, FF starts to decrease to around 34% for all device structures. In the quest for achieving peak efficiency, the optimal N_t within the PAL was identified as $1 \times 10^{12} \text{ cm}^{-3}$ across all ETLs. This comprehensive analysis underscores the critical importance of managing defect density within the absorber layer for optimizing and maintaining the performance and stability of PSCs over time.

3.6. Influence of the ETL Properties. **3.6.1. Influence of the ETL Donor Doping Density (N_D).** The variations in PV parameters, including PCE, V_{oc} , J_{sc} , and FF, in relation to the N_D of TiO_2 -, PCBM -, WS_2 -, SnO_2 -, ZnO -, and IGZO as ETLs are depicted in Figure 17. The N_D for all ETLs was adjusted within the range of 1×10^{13} to $1 \times 10^{20} \text{ cm}^{-3}$. All PV parameters remained stable when $N_D \leq 1 \times 10^{16} \text{ cm}^{-3}$, and beyond this threshold, a gradual increase in all PV parameters was observed. J_{sc} values for TiO_2 -, WS_2 -, ZnO -, and IGZO ETLs

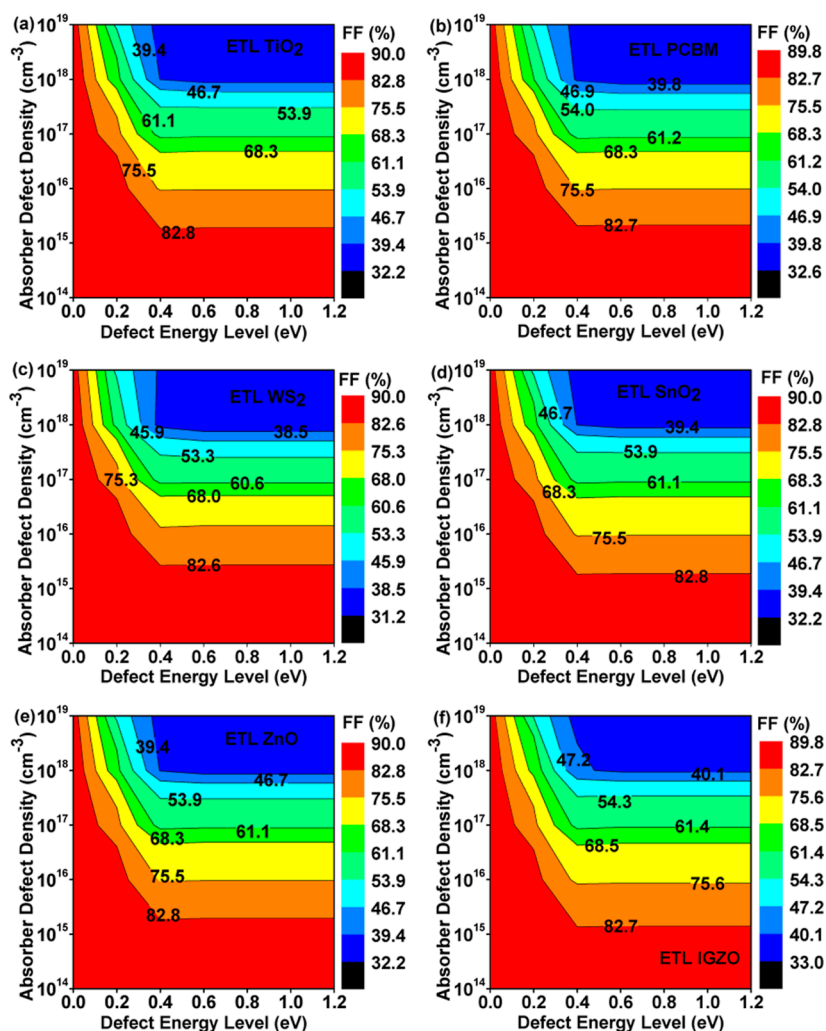


Figure 11. Contour plots depict the influence of defect energy level and absorber defect density on FF for different ETLs.

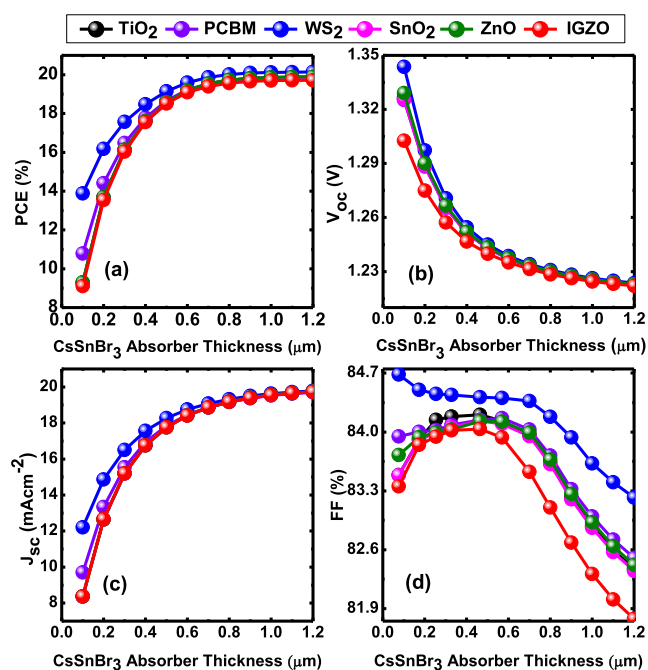


Figure 12. Influence of CsSnBr₃ layer thickness on PV performance.

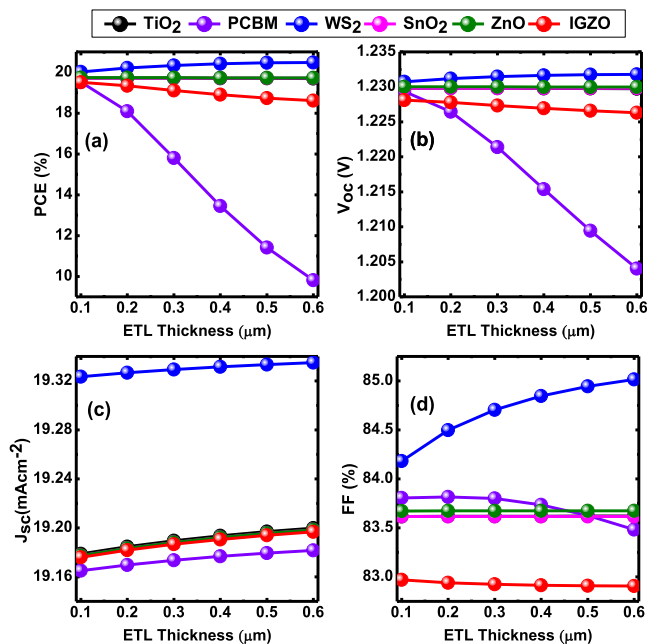


Figure 13. Influence of ETL thickness on PV performance.

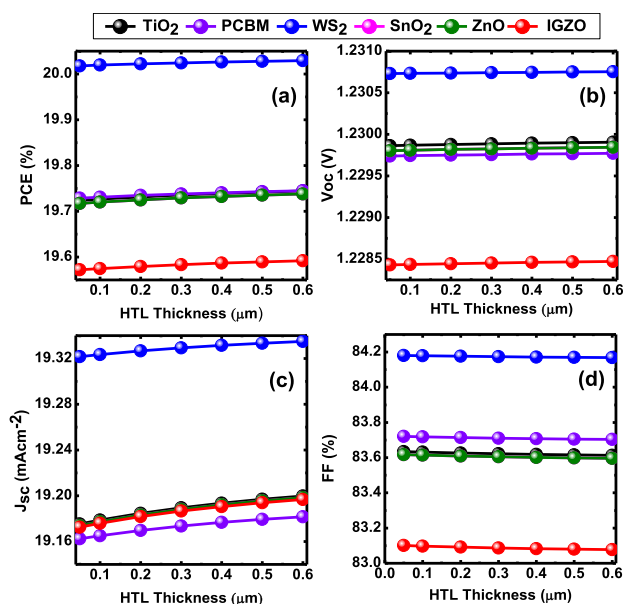


Figure 14. Influence of HTL thickness on PV performance.

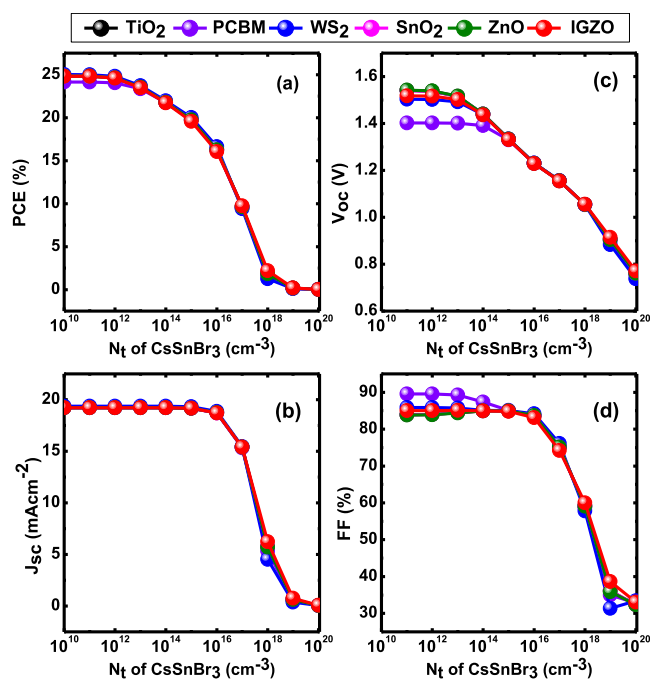


Figure 16. Influence of PAL defect density on PV performance.

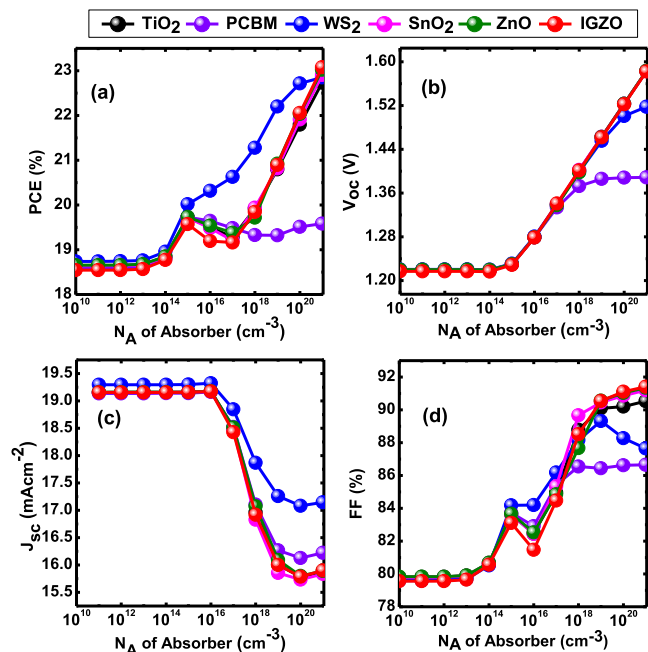


Figure 15. Influence of PAL Acceptor Doping Density on PV performance.

remained constant with varying N_D , while PCBM and SnO₂ exhibited a declining trend as N_D ranged from 1×10^{18} to 1×10^{20} cm⁻³. For $N_D \geq 1 \times 10^{19}$ cm⁻³, all PV parameters saturated across all ETLs. The increase in N_D led to an enhancement in PCE, V_{oc} , and FF due to enhanced charge extraction and transportation at the ETL/absorber interfaces. Conversely, lower N_D values resulted in reduced values of these parameters due to increased series resistance. The J_{sc} tends to decrease or remain constant after reaching a certain value of N_D (Figure 17c). The optimum N_D values are 1×10^{18} cm⁻³ for TiO₂, PCBM, WS₂, SnO₂, ZnO, and 1×10^{19} cm⁻³ for IGZO, respectively.

3.6.2. Influence of the ETL Defect Density. Figure 18 depicts the impact of the defect density (N_t) of the ETL on the

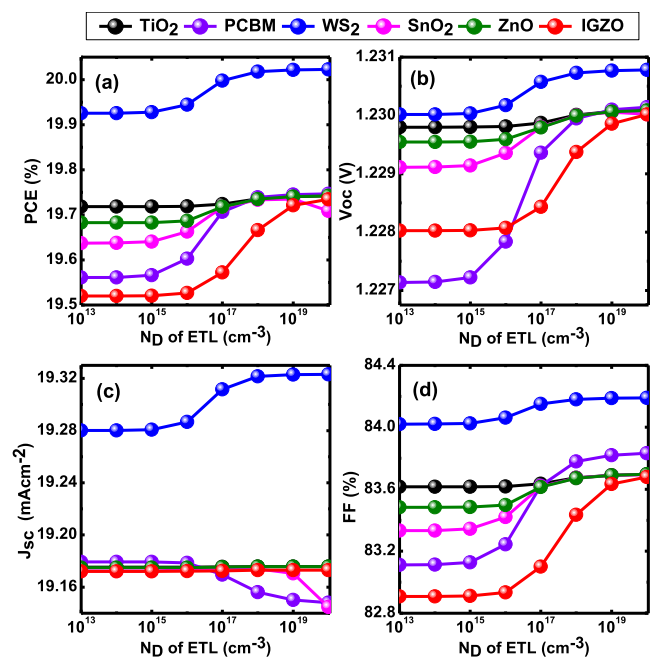


Figure 17. Influence of the ETL donor doping density on PV performance.

PCE, V_{oc} , J_{sc} , and FF. To achieve the targeted N_t for the ETL, the N_t was finetuned within the range of 1×10^{11} to 1×10^{17} cm⁻³. As illustrated in Figure 18, nearly all performance parameters, PCE, V_{oc} , J_{sc} , and FF, remained constant for all ETLs as the N_t of the ETL increased, except for PCBM, where the PV parameters decreased when N_t surpassed 1×10^{15} cm⁻³. WS₂ and ZnO, functioning as ETLs, exhibited the highest V_{oc} value at 1.23 V, while other ETLs showed a V_{oc} value of approximately 1.2 V. The FF value for the majority of ETLs remained constant, except for WS₂ and PCBM, which exhibited a slight decrease after reaching 1×10^{15} cm⁻³. According to the simulated data, the optimized N_t is

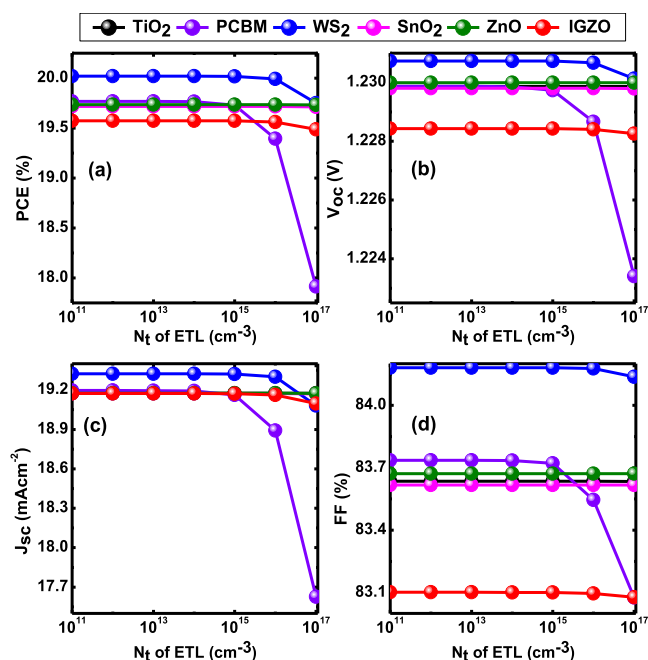


Figure 18. Influence of the ETL defect density on PV performance.

determined to be $1 \times 10^{15} \text{ cm}^{-3}$ for all six sets of PSC structures.

3.7. Influence of the HTL Properties. **3.7.1. Influence of the HTL Acceptor Doping Density (N_A).** Figure 19 illustrates

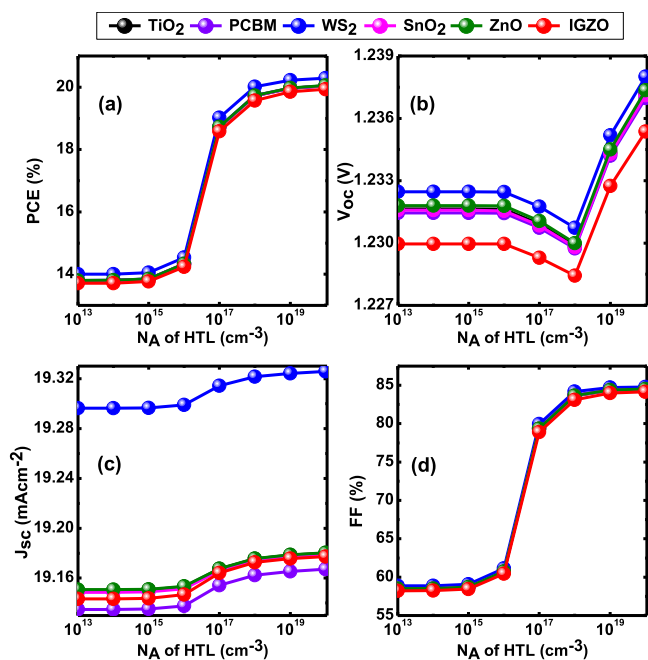


Figure 19. Influence of the HTL acceptor doping density on PV performance.

the variation in PV parameters with an increase in N_A of the HTL for different ETL-based structures. N_A is adjusted within the range of 1×10^{13} to $1 \times 10^{20} \text{ cm}^{-3}$ for all device structures under investigation.

In Figure 19a, it is noticeable that PCE values for different ETLs exhibit relatively lower values when $N_A < 1 \times 10^{16} \text{ cm}^{-3}$ due to a diminished electric field, leading to reduced carrier

extraction. Moreover, as N_A exceeds $1 \times 10^{16} \text{ cm}^{-3}$, PCE shows an increasing trend in all device configurations. The PCE of TiO_2 , PCBM, WS_2 , SnO_2 , IGZO, and ZnO increased from 13.79 to 20.04%, 13.8 to 20.03%, 13.99 to 20.28%, 13.78 to 20.03%, 13.8 to 20.05%, and 13.7 to 19.93%, respectively, with the rise in N_A . A similar trend is observed for the FF values in all PSCs (Figure 19d), where FF increases as N_A varies from 1×10^{15} to $1 \times 10^{16} \text{ cm}^{-3}$. Like PCE, the FF of TiO_2 , PCBM, WS_2 , SnO_2 , IGZO, and ZnO increased from 58.48 to 84.47%, 58.56 to 84.51%, 58.86 to 84.78%, 58.47 to 84.46%, 58.50 to 84.49%, and 58.21 to 84.15%, respectively. The J_{sc} values for all ETLs associated with PSCs exhibit a gradual increase with the growing N_A (Figure 19c). The V_{oc} for all device structures rises when $N_A \leq 1 \times 10^{15} \text{ cm}^{-3}$, and when $1 \times 10^{15} \text{ cm}^{-3} < N_A < 1 \times 10^{17}$, V_{oc} decreases. For $N_A > 1 \times 10^{19} \text{ cm}^{-3}$, V_{oc} again increases as the N_A rises from 1×10^{15} to $1 \times 10^{17} \text{ cm}^{-3}$. The optimum N_A is found to be $1 \times 10^{19} \text{ cm}^{-3}$ for all ETL-based devices.

3.7.2. Influence of the HTL Defect Density. The defect density (N_t) of the HTL ranges from 1×10^{11} to $1 \times 10^{17} \text{ cm}^{-3}$, as illustrated in Figure 20. The PV parameters of various

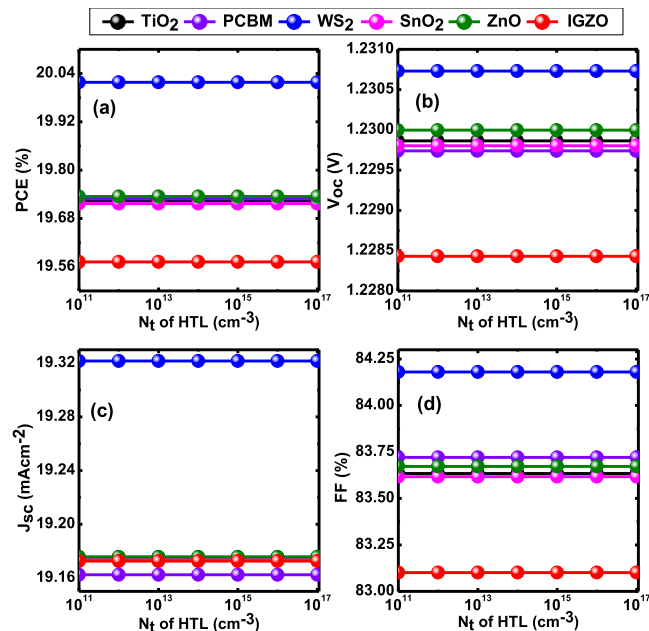


Figure 20. Influence of the HTL defect density on PV performance.

ETLs remained constant as the N_t of the HTL increased to $1 \times 10^{17} \text{ cm}^{-3}$. Consequently, the performance of six sets of CsSnBr_3 -based PSCs exhibited an insignificant impact with the increasing N_t . The FF displayed a similar trend to the PCE for all ETLs as the N_t of the HTL is varied. The V_{oc} and J_{sc} values remained constant across all ETL device configurations as the N_t of the HTL increased. Therefore, the increase in N_t had an insignificant effect on the device configurations. The optimized value for N_t is established at $1 \times 10^{15} \text{ cm}^{-3}$ for all the six device structures for further enhancement.

3.8. Influence of Interface Properties. **3.8.1. Influence of CsSnBr_3 /ETL Interface Defect Density on PV Performance.**

Figure 21 depicts the alterations in PCE, V_{oc} , J_{sc} , and FF in relation to the varying CsSnBr_3 /ETL interface defect density (N_i). To observe the fluctuations in these PV parameters, the N_i value was adjusted from 1×10^{10} to $1 \times 10^{20} \text{ cm}^{-2}$. When $N_i \leq 1 \times 10^{13} \text{ cm}^{-2}$, all PV parameters remain stable;

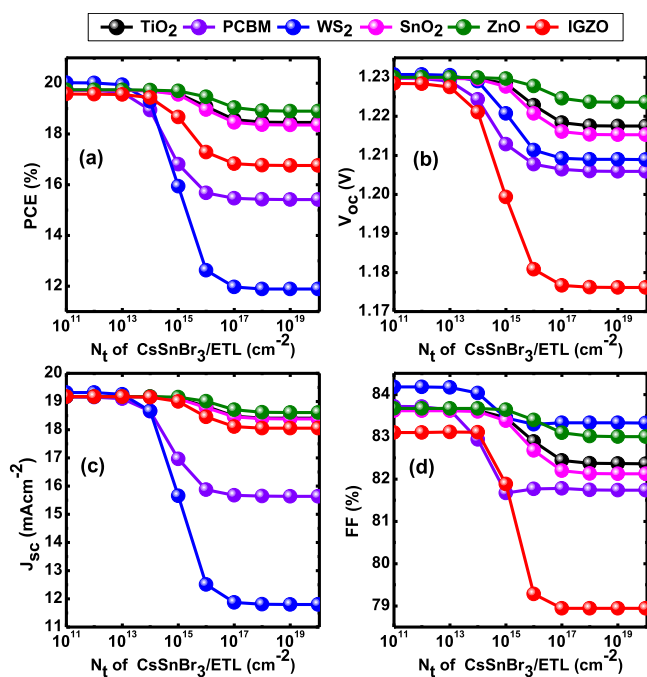


Figure 21. Influence of CsSnBr₃/ETL interface defect density on PV performance.

however, surpassing $1 \times 10^{13} \text{ cm}^{-2}$, all PV parameters commence a decline. The PCE of TiO₂, PCBM, WS₂, SnO₂, IGZO, and ZnO decreases from 19.72 to 18.45%, 19.72 to 15.41%, 20.01 to 11.88%, 19.71 to 18.34%, 19.73 to 18.90%, and 19.57 to 16.76%, respectively, with an increase in N_t of the CsSnBr₃/ETL interface. The V_{oc} of TiO₂, PCBM, WS₂, SnO₂, IGZO, and ZnO also decreases from 1.229 to 1.217 V, 1.229 to 1.205 V, 1.23 to 1.208 V, 1.229 V to 1.215 V, 1.23 to 1.223 V, and 1.228 to 1.176 V, respectively, as the N_t of the CsSnBr₃/ETL interface increases. The limit on V_{oc} imposed by recombination at the interface is determined by eq 15 as⁴²

$$V_{oc} = \frac{\phi_B}{q} - \frac{\eta KT}{q} \ln \left(\frac{q N_t S_{it}}{J_{sc}} \right) \quad (15)$$

where S_{it} is the interface recombination velocity and ϕ_B is the effective barrier height. Equation 15 reveals that high N_t will generate S_{it} leading to a decrease in V_{oc} . To achieve high values of V_{oc} , it is desirable to maximize the effective barrier height at the CsSnBr₃/ETL interface, preventing the recombination of charge carriers.

The J_{sc} of TiO₂, PCBM, WS₂, SnO₂, IGZO, and ZnO decreases from 19.17 to 18.40%, 19.16 to 15.63%, 19.32 to 11.79%, 19.17 to 18.38%, 19.17 to 18.60%, and 19.17 to 18.05%, respectively, with an increase in the N_t of the CsSnBr₃/ETL interface. Among all ETLs, WS₂ PV performance is significantly impacted with an increase in N_t . The FF of all ETL-based devices decreases with an increase in N_t of the CsSnBr₃/ETL interface (Figure 21d). The simulation results highlight that the optimal N_t for device simulation is $1 \times 10^{13} \text{ cm}^{-2}$.

3.8.2. Influence of Cu₂O/CsSnBr₃ Interface Defect Density on PV Performance. Numerous defect states commonly coexist at the interface between the absorber and charge transport layers, and the performance of the PSC is significantly influenced by the quality of this interface. Previous research indicates that the N_t on the irradiation side has a more

pronounced impact on PV performance compared to the N_t at the rear interface.⁴³ Figure 22 depicts the variations in PV cell

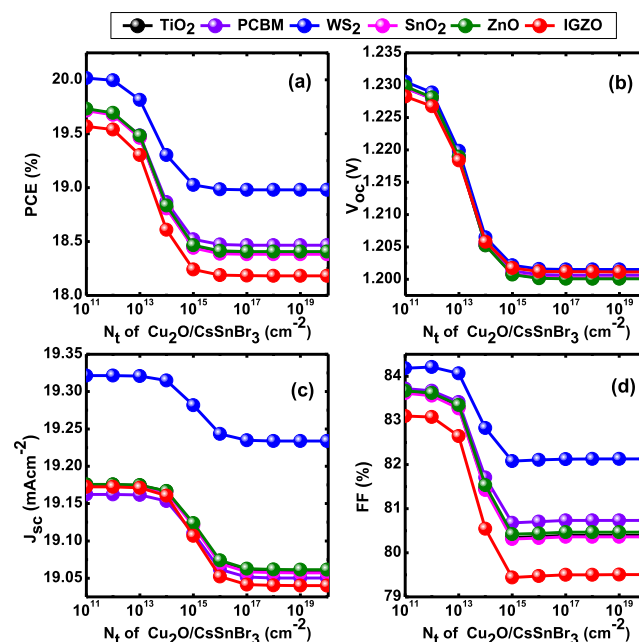


Figure 22. Influence of Cu₂O/CsSnBr₃ interface defect density on PV performance.

parameters, including PCE, V_{oc} , J_{sc} , and FF, in relation to the varying N_t at the Cu₂O/CsSnBr₃ interface. All PV parameters exhibit a decreasing trend as the N_t is varied from 1×10^{11} to $1 \times 10^{20} \text{ cm}^{-2}$. When N_t is $\leq 1 \times 10^{13} \text{ cm}^{-2}$, all PV parameters remain stable; however, beyond $1 \times 10^{13} \text{ cm}^{-2}$, a decline is observed in all parameters. Simulation data reveals that an increase in N_t leads to a rise in recombination rate and a decrease in PCE. Higher N_t at the Cu₂O/CsSnBr₃ interface results in increased trap and recombination centers, thereby diminishing PSC performance. The simulation results highlight that the optimal N_t for device simulation is $1 \times 10^{13} \text{ cm}^{-2}$.

3.9. Influence of Capacitance (C–V) and Mott–Schottky (M–S) Analysis on PV Performance. The Mott–Schottky (M–S) analysis serves as a valuable tool for comprehending the electrical properties of semiconductor materials and provides insights into the behavior of PSCs. This method is widely recognized and reliable, specifically for evaluating the built-in potential (V_{bi}) and elucidating the operational functions and doping levels of an electrode.^{24,31} In the M–S plot, the V_{bi} is typically represented by the x -axis intercept, while the slope can estimate the doping density of the absorber layer. In all the investigated PSC structures, V_{bi} was determined to be 1.3 V through the extrapolation of the x -intercept on the M–S plot. Figure 23 illustrates the M–S plot for the device, considering six distinct ETLs. It is evident that all of them display a negative slope, indicating the p-type conductivity of the device.⁴⁴ A higher flat band potential results in a reduced energy barrier for carrier transfer at the interface. Consequently, a lower voltage is needed to facilitate the transportation of charge carriers across the interface.⁴⁵ In simpler terms, a device with the greatest V_{bi} will yield the highest V_{oc} . These findings align with the simulated performance results of the device using various ETLs. Equation 16 gives the depletion capacitance per unit area as

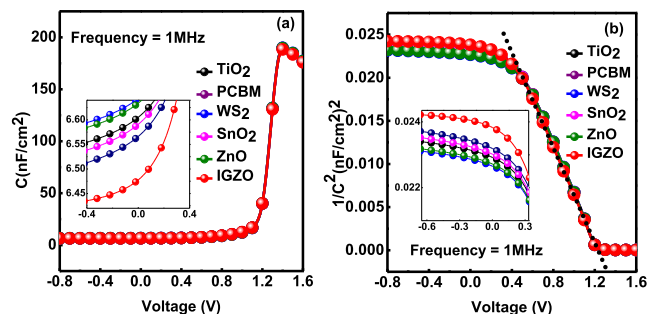


Figure 23. C–V and M–S plot for six optimized structures.

$$\frac{1}{C_j^2} = \frac{2}{q\epsilon_s\epsilon_0 N_D} (V_{bi} - V) \quad (16)$$

Accordingly, if the slope of M–S plot is

$$\text{slope} = \frac{\partial}{\partial V} \left(\frac{1}{C_j^2} \right) \quad (17)$$

Then, the value of N_D follows from

$$N_D = \frac{2}{q\epsilon_s\epsilon_0} \frac{-1}{\text{slope}} \quad (18)$$

This study explores the variation of capacitance and M–S concerning applied voltage in six optimized PSC device structures. These structures utilize Cu_2O as the HTL and TiO_2 , PCBM, WS_2 , SnO_2 , ZnO, and IGZO as the corresponding ETLs, with CsSnBr_3 serving as the absorber layer. Figure 23a illustrates a nonlinear increase in capacitance with applied voltage, while Figure 23b shows a noticeable decrease in the M–S plot as the voltage increases. This phenomenon can be attributed to several influential factors. First, variations in voltage induce changes in charge carrier density, thereby initiating adjustments in capacitance. Additionally, the influence of defect states emerges as a crucial determinant in capacitance dynamics, with voltage fluctuations leading to shifts in the occupancy and behavior of these states. The impact of electric field effects is noteworthy, as the increased applied voltage establishes a more robust electric field, affecting charge distribution and contributing to the observed alterations in capacitance. Furthermore, the broadening of the depletion region at the semiconductor surface with increasing voltage serves as an additional contributing factor, playing a pivotal role in shaping the overall capacitance dynamics. PSCs utilizing TiO_2 , PCBM, WS_2 , SnO_2 , ZnO, and IGZO exhibited the maximum capacitance values, measuring 189.79, 189.57, 190.22, 189.76, 189.92, and 188.53 nF/cm^2 , respectively. Among these, the WS_2 -based PSC exhibited the highest capacitance, while the IGZO-based PSC showed the lowest capacitance value as the voltage increases.

3.10. J–V and QE Characteristics. Figure 24a,b illustrates the before and after optimization stages of the J–V curve for six considered PSCs. Before optimization, PCBM demonstrated a J_{sc} of 19.16 mA cm^{-2} , WS_2 exhibited 19.32 mA cm^{-2} , while all other devices relying on ETLs displayed a J_{sc} of 19.17 mA cm^{-2} . The V_{oc} for WS_2 and ZnO was 1.23 V, whereas for all other devices, it stood at 1.22 V. In contrast, during the final optimization, the J_{sc} for TiO_2 decreased to 18.96 mA cm^{-2} , while that for IGZO remained constant. PCBM, WS_2 , SnO_2 , and ZnO showcased improved J_{sc} values of 19.21, 19.6, 19.22,

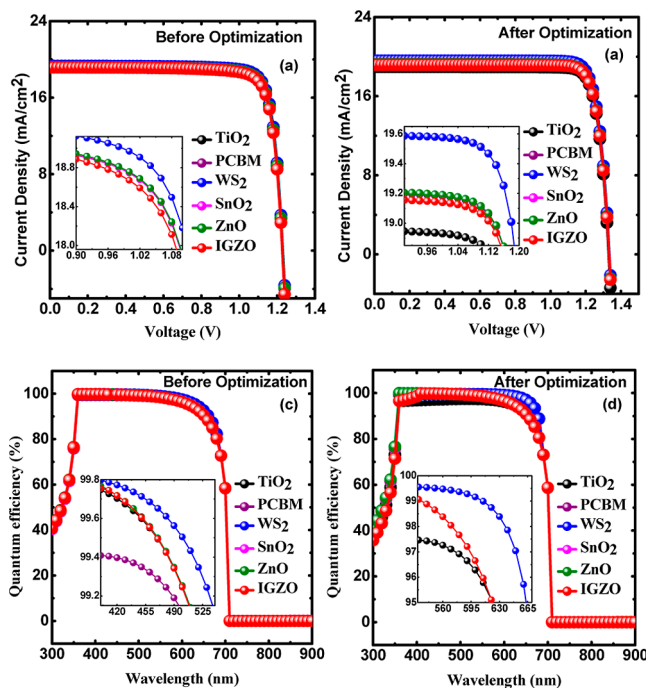


Figure 24. Before and after optimization stages of the (a,b) J–V curve and (c,d) QE curves.

and 19.22 mA cm^{-2} , respectively. Following the final optimization process, all ETL-based devices exhibited a V_{oc} of approximately 1.33 V. Figure 24c,d depicts the variation of QE concerning the wavelength for CsSnBr_3 -based PSCs during both before and after optimization process. The QE, representing the proportion of charge carriers generated by a SC to incident photons, is influenced by the light wavelength. The QE typically improves as the absorber thickness increases, allowing for greater photon absorption. Observations indicate that QE remains nearly constant in the wavelength range of 360–600 nm and decreases beyond this range under both initial and final optimized conditions. Figure 24c demonstrates that under initial (before) optimization conditions, the QE is almost 100% for all six structures at a wavelength of about 360 nm. After final optimization condition, PSCs based on ITO/ETL/ CsSnBr_3 / Cu_2O /Au exhibit a modest decline in QE values in comparison to the before optimization, as can be seen in Figure 24d, suggesting a negligible loss in QE. The bandgap of the light-harvesting layer ($E_{g(\text{CsSnBr}_3)} = 1.75$ eV) is aligned with the initial and final optimization curves, where no device structure (out of six) shows QE beyond 700 nm.⁴⁴

3.11. Comparative Study between SCAPS-1D and wxAMPS Results. To enhance the robustness and credibility assessment of the results obtained from the SCAPS-1D simulator, we conducted additional evaluations using simulations performed with the wxAMPS program. Table 3 provides a comprehensive overview of the PV parameters for six different structures of CsSnBr_3 -based devices. For the TiO_2 -based PSC, the SCAPS-1D simulator yields a PCE of 21.64%, whereas the wxAMPS simulator records a PCE of 20.60%. Similarly, employing PCBM as the ETL results in a PCE of 21.94% in SCAPS-1D, while the wxAMPS simulator shows a PCE of 20.42%. The WS_2 -based PSC demonstrates a PCE of 22.43% in SCAPS-1D, and a slightly lower PCE of 21.85% in the wxAMPS simulator. Furthermore, employing SnO_2 , ZnO, and IGZO as ETLs in CsSnBr_3 -based PSCs results in PCE

Table 3. Comparative Study between SCAPS-1D and wxAMPS Results

device structure	software	PCE (%)	V_{oc} (V)	J_{sc} (mA cm ⁻²)	FF (%)
ITO/TiO ₂ /CsSnBr ₃ /Cu ₂ O/Au	SCAPS-1D	21.64	1.33	18.96	85.78
	wxAMPS	20.60	1.23	19.95	83.92
ITO/PCBM/CsSnBr ₃ /Cu ₂ O/Au	SCAPS-1D	21.94	1.33	19.22	85.65
	wxAMPS	20.42	1.23	19.67	84.37
ITO/WS ₂ /CsSnBr ₃ /Cu ₂ O/Au	SCAPS-1D	22.43	1.33	19.59	85.80
	wxAMPS	21.85	1.24	20.35	86.58
ITO/SnO ₂ /CsSnBr ₃ /Cu ₂ O/Au	SCAPS-1D	21.94	1.33	19.22	85.65
	wxAMPS	20.68	1.23	19.68	85.41
ITO/ZnO/CsSnBr ₃ /Cu ₂ O/Au	SCAPS-1D	21.94	1.33	19.22	85.65
	wxAMPS	20.37	1.24	19.34	84.93
ITO/IGZO/CsSnBr ₃ /Cu ₂ O/Au	SCAPS-1D	21.88	1.33	19.17	85.65
	wxAMPS	20.14	1.23	19.38	84.47

values of 21.94, 21.94, and 21.88%, respectively, in the SCAPS-1D simulator. Conversely, the wxAMPS simulator shows slightly reduced PCE values of 20.68, 20.37, and 20.14% for SnO₂, ZnO, and IGZO, respectively. Concerning the V_{oc} , for TiO₂-based structure, the SCAPS-1D simulator indicates a V_{oc} of 1.33 V, while the wxAMPS simulator records a slightly lower value of 1.23 V. Both simulators consistently exhibit almost similar patterns of results for V_{oc} across all device configurations. Comparing the PV parameters of the six PSC structures reveals that SCAPS-1D and wxAMPS simulators yield nearly identical results, except for PCE and FF, where wxAMPS exhibits slightly lower values than SCAPS-1D. The results obtained from both simulators contribute valuable insights into the charge transport materials and absorber material suitable for optimal device configuration. The consistent agreement between the two simulators for all six sets of optimum device configurations enhances the reliability of the presented results for further investigation. Minor discrepancies in simulation results between SCAPS-1D and wxAMPS can be primarily ascribed to variations in numerical algorithms, models, grid resolution, meshing, boundary conditions, simulation settings, convergence criteria, and software implementation. These diverse elements collectively contribute significantly to the observed differences in outcomes generated by the two simulators.

3.12. Comparative Study of SCAPS-1D Results with Prior Research Work. Among the 70 structures considered, we focused on further investigating and optimizing the performance of the top six structures with the following architectures: ITO/TiO₂/CsSnBr₃/Cu₂O/Au, ITO/PCBM/CsSnBr₃/Cu₂O/Au, ITO/WS₂/CsSnBr₃/Cu₂O/Au, ITO/SnO₂/CsSnBr₃/Cu₂O/Au, ITO/ZnO/CsSnBr₃/Cu₂O/Au, and ITO/IGZO/CsSnBr₃/Cu₂O/Au. The outcomes of this optimization study are outlined in Table 4, providing a basis for a comprehensive and equitable comparison. Table 5 provides a comparative analysis of the PV parameters for the six presented device configurations with recently published optimal configurations. The ITO/TiO₂/CsSnBr₃/Spiro-OMeTAD/Au heterostructure has been documented with the highest experimental PCE at 10.46%. Simulation has previously yielded the highest PCE of 17.94%. This study presents, for the first time, a record-breaking PCE >22% for the device structure employing CsSnBr₃ as the absorber layer.

Table 4. Optimization Parameters: Thickness, N_A (cm⁻³), N_D (cm⁻³), and N_t (cm⁻³) of the HTL (Cu₂O), Absorber Layer CsSnBr₃, and Different ETLs

set	layers	thickness (μm)	acceptor density, N_A (cm ⁻³)	donor density, N_D (cm ⁻³)	defect density, N_t (cm ⁻³)
set-1	TiO ₂	0.3	1×10^{19}	1×10^{18}	1×10^{14}
	Cu ₂ O	0.05	1×10^{15}		1×10^{15}
	CsSnBr ₃	0.8			1×10^{14}
set-2	PCBM	0.1	1×10^{19}	1×10^{18}	1×10^{14}
	Cu ₂ O	0.05	1×10^{15}		1×10^{15}
	CsSnBr ₃	0.8			1×10^{14}
set-3	WS ₂	0.5	1×10^{19}	1×10^{18}	1×10^{14}
	Cu ₂ O	0.05	1×10^{15}		1×10^{15}
	CsSnBr ₃	0.8			1×10^{14}
set-4	SnO ₂	0.1	1×10^{19}	1×10^{18}	1×10^{14}
	Cu ₂ O	0.05	1×10^{15}		1×10^{15}
	CsSnBr ₃	0.8			1×10^{14}
set-5	ZnO	0.1	1×10^{19}	1×10^{18}	1×10^{14}
	Cu ₂ O	0.05	1×10^{15}		1×10^{15}
	CsSnBr ₃	0.8			1×10^{14}
set-6	IGZO	0.1	1×10^{19}	1×10^{19}	1×10^{14}
	Cu ₂ O	0.05	1×10^{15}		1×10^{15}
	CsSnBr ₃	0.8			1×10^{14}

4. CONCLUSIONS

SCAPS-1D simulations were utilized to systematically improve the PV characteristics of Pb-free CsSnBr₃ PSCs, and the key findings are summarized as follows:

Optimal PCEs were identified through simultaneous adjustments to the absorber layer thickness and defect density (N_t). The most favorable PCEs were attained with absorber thickness in the range of 0.6–1.2 μm and N_t set at 1×10^{13} cm⁻³. In the case of PCBM, the lowest and highest PCEs were observed with WS₂. Meanwhile, varying the defect energy level (E_t) alongside N_t revealed that the best PCEs occurred with defect densities ranging from 1×10^{14} to 1×10^{16} cm⁻³ and E_t set at 0.2 eV. The PV characteristics are significantly influenced by the CsSnBr₃ thickness and ETLs, while the Cu₂O thickness has a negligible effect. Doping densities in the CsSnBr₃ layer, ETL, and Cu₂O (HTL) significantly influence the PV performance. The N_t of the charge transport materials also has a significant effect on the PV parameters except for HTL. The dopant density in the CTM affects the PSC performance. PV parameters deteriorate as the N_t at the CsSnBr₃/ETL and HTL/CsSnBr₃ interfaces increases. The optimal N_t for both interfaces were found to be 1×10^{13} cm⁻³ for the best PSC

Table 5. Comparative Study of SCAPS-1D Results with Prior Research Work^a

type	device structure	PCE (%)	V _{oc} (V)	J _{sc} (mA cm ⁻²)	FF (%)	year	ref
E	FTO/TiO ₂ /CsSnBr ₃ /Spiro-OMeTAD/Au	0.10	0.19	1.57	34	2015	27
E	FTO/TiO ₂ /CsSnBr ₃ :SnF ₂ /Spiro-OMeTAD/Au	0.95	0.41	3.99	58	2015	27
E	FTO/TiO ₂ /CsSnBr ₃ :SnF ₂ /Spiro-OMeTAD/Au	2.04	0.4	9.1	56	2016	11
E	FTO/TiO ₂ /CsSnBr ₃ /Spiro-OMeTAD/Au	0.01	0.1	0.4	38	2016	11
E	ITO/TiO ₂ /CsSnBr ₃ /Spiro-OMeTAD/Au	10.46	0.85	21.23	58	2016	33
E	ITO/MoO ₃ /CsSnBr ₃ :SnF ₂ /C ₆₀ /BCP/Ag	0.5	0.45	2.1	52	2016	16
E	ITO/MoO ₃ /CsSnBr ₃ :SnF ₂ /C ₆₀ /BCP/Ag	0.55	0.407	2.4	55	2016	16
E	ITO/MoO ₃ /CsSnBr ₃ :SnF ₂ /C ₆₀ /BCP/Ag	0.26	0.41	1.6	38	2016	16
E	FTO/TiO ₂ /CsSnBr ₃ /MoO ₃ (20 nm)/Al	0.05	0.08	2.60	25	2021	21
E	FTO/TiO ₂ /CsSnBr ₃ /MoO ₃ (30 nm)//Al	1.5	0.38	11.96	34	2021	21
E	FTO/TiO ₂ /CsSnBr ₃ /MoO ₃ (40 nm)/Al	0.58	0.28	5.86	35	2021	21
T	FTO/SnO ₂ /CsSnBr ₃ /CuI/Au	17.06	1.19	16.94	84.19	2023	46
T	FTO/TiO ₂ /CsSnBr ₃ /P3HT/Au	17.94	1.3	18	74.5	2023	24
T	ITO/TiO ₂ /CsSnBr ₃ /Cu ₂ O/Au	21.64	1.330	18.96	85.78		^b
T	ITO/PCBM/CsSnBr ₃ /Cu ₂ O/Au	21.94	1.332	19.22	85.65		^b
T	ITO/WS ₂ /CsSnBr ₃ /Cu ₂ O/Au	22.43	1.333	19.59	85.80		^b
T	ITO/SnO ₂ /CsSnBr ₃ /Cu ₂ O/Au	21.94	1.332	19.22	85.65		^b
T	ITO/ZnO/CsSnBr ₃ /Cu ₂ O/Au	21.94	1.332	19.22	85.65		^b
T	ITO/IGZO/CsSnBr ₃ /Cu ₂ O/Au	21.88	1.332	19.17	85.65		^b

^aE—experimental, T—theoretical. ^bThis work.

performance. The optimized ITO/WS₂/CsSnBr₃/Cu₂O/Au device structure achieved the highest PCE of 22.43%, V_{oc} of 1.33 V, J_{sc} of 19.59 mA/cm², and FF of 85.80%. The PCBM-, SnO₂-, ZnO-based devices showed a PCE of 21.94%. Meanwhile, TiO₂- and IGZO-based devices exhibited PCEs of 21.64 and 21.88%. Additionally, the simulated outcomes generated by SCAPS-1D were replicated and confirmed through validation using the wxAMPS device simulator.

■ ASSOCIATED CONTENT

SI Supporting Information

The Supporting Information is available free of charge at <https://pubs.acs.org/doi/10.1021/acs.energyfuels.4c00953>.

Material parameters of different HTLs used in the simulation and performance of 70 different configurations with additional supplementary results (PDF)

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<https://pubs.acs.org/doi/10.1021/acs.energyfuels.4c00953>

Notes

The authors declare no competing financial interest.

■ ACKNOWLEDGMENTS

The authors express their sincere gratitude to Marc Burgelman and his colleagues at the University of Gent, Belgium for supplying the SCAPS simulation package. They would also like

to thank Professor Fonash from Penn State University for providing the wxAMPS simulation package.

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